

FuturePlus Systems Adds
Serial RapidIO Protocol Analyzer

New Probe Offers 3.125 Gb/s SRIO Protocol Analysis, Peer Product Combines SRIO and 2.5 Gb/s PCI Express

COLORADO SPRINGS, Colo. January 2006 -- FuturePlus Systems today introduced the FS4410, a low-cost Serial RapidIO (SRIO) protocol analysis probe for use with Agilent Technologies logic analyzers. It will be used by designers involved in development of computers and peripherals incorporating the SRIO architecture. The FS4410 allows non-intrusive probing of SRIO buses at a data rate of 3.125 Gb/s.

The FS4410 connects to the system under test with a full-size or a half-size mid-bus probe to monitor bus traffic in real time. A flying-lead probe is also offered for applications requiring maximum passive-probing flexibility.

Why SRIO?

“SRIO technology promises to meet the widespread, growing need for reliability, increased bandwidth, lower latency and faster bus speeds required by today’s wireless, networking, scientific and other equipment,” said Siegfried Gross, vice president and general manager of Agilent’s Digital Verification Solutions Division. “As SRIO’s high performance and cost effectiveness continue to spur new implementations, designers will face more challenging problems in the debug and verification phase of product development. The FS4410 analysis module, combined with Agilent’s powerful logic analysis technology, will boost engineering productivity while providing the most competitive price-to-performance value available.”

FS4410 Feature Highlights

- 8b/10b data acquisition at 3.125, 2.5 or 1.25 Gb/s
- x1 lane and x4 lane modes.
- Supports debug of physical, transport and logical layers (messaging, I/O and streaming)
- Packet spreading of X1 link data to 4-column format
- Supports most Agilent 16700 and 16900 logic analyzer modules
- Optional modification can add user-selectable PCI Express analysis capability.
- Integral USB port for probe control from a PC

Compatible With Legacy Logic Analyzer Platforms While many newer bus analysis probes are designed for use only with the newest generation of logic analysis modules, the FS4410 is compatible with most 16700- and 16900-series modules.

Three Software Components Simplify Analysis, Speed Setup The analysis probe includes software that decodes and displays bus data as protocol, hexadecimal or binary data.

Configuration files are also provided to facilitate quick logic-analyzer setup. The FS4410 also includes the new Transaction Viewer software, which presents bus transactions in a graphical, horizontal-bar view that allows users to drill down to see transaction details.

Peer Product, FS4411 Combines User-selectable SRIO and PCI Express Protocol Analysis

In addition to the FS4410, FuturePlus also introduced the FS4411 SRIO Plus PCI Express (PCIe) analysis probe. The FS4411 incorporates all of the SRIO analysis features of the FS4410, as well as the PCIe analysis features of the FS4400 probe, introduced in late 2005. Users can switch between SRIO and PCIe analysis modes with the included Probe Manager software.

U.S. Price and Delivery

FS4410 Serial RapidIO Analysis Probe, \$25,000, less probe adapter cables.

FS4411 Serial RapidIO Plus PCI Express Analysis Probe, \$35,000, less probe adapter cables.

FS1031 Full size, Agilent soft touch footprint X1, X2 or X4, (Agilent N4221A), \$5,201

FS1032 1/2 size, Agilent soft touch footprint for X1, X2 or X4, \$4,000

FS1036 Flying leads (Agilent N4221F), \$6,500 Delivery is six weeks ARO.

About FuturePlus Systems FuturePlus Systems Corporation is a privately held manufacturer of bus protocol analysis tools for the computer industry. The company is an Agilent Technologies channel partner. FuturePlus Systems products are also supported by authorized resellers in Canada, Taiwan, Japan, Italy, United Kingdom and most European countries. More information about FuturePlus Systems Corporation may be found on the internet at www.futureplus.com.

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