FS2331 DDR 333 Analysis Probe Press Release

DDR333 Probe from FuturePlus Systems Corp.
Confronts Design Challenges of New, Faster Memory-Busses.

INTEL DEVELOPER FORUM, SAN JOSE, Calif., Sept., 09, 2002 -- FuturePlus® Systems Corp. today introduced the FS2331 DDR333 SDRAM memory-bus analysis probe. This new tool will meet or exceed the requirements of computer designers who are responsible for debug, validation, compliance, and performance testing of computer systems and peripherals that employ the DDR333 specification.

The new probe provides a mechanical and electrical interface between the bus under test and an Agilent logic analyzer. It is designed to plug directly into the user's DDR memory socket, while connecting to an Agilent logic analyzer via cables. Included transaction-decoder software translates the acquired data into bus transactions. The product also includes software that automatically configures the logic analyzer channels to match the proper signals from the DDR bus.

Key Features of the FS2331
• Complete and accurate 400 MT/s state analysis and 4 GHz timing analysis.
• Transaction decoder software executes on the Agilent logic analyzer.
• Quick and easy connection between the DDR bus connector and Agilent logic analyzers.
• Passive design enables eye diagram measurements (voltage vs. time) across entire bus using Agilent's eye-scan technology
• Automatic 10 ps alignment of clock/strobes with data using eye finder
• Support for SODIMM and embedded DDR SDRAMs
The Latest of an Array of New Analysis Probes from FuturePlus Systems.

The FS2331 is the latest of several products introduced by FuturePlus Systems over the past year. Designed to meet unprecedented design challenges presented by faster, more compact bus technologies, the new probe products include:

• FS2331 DDR333 memory bus analysis probe
• FS2240 HyperTransport link analysis probe
• FS2229 AGP3.0 (8X) bus analysis probe
• FS4120 USB 2.0 bus analysis probe

Addressing the Emerging Needs of Designers
Simply having a powerful acquisition and processing engine is not enough in the world of today's complex systems. Designers also require high-fidelity methods of connection that do not mask or inject problems such as reflections or signal loading. Analysis tools also must provide protocol views of the data, rather than showing only hex or binary information. Since bus, processor, and memory systems must operate together, it has also become crucial to cross trigger and time-correlate data and markers between different devices in high-

performance systems. FuturePlus analysis probes combine with Agilent logic-analysis systems to provide this synergy.

Price and Delivery
The FS2331 has a U.S. list price of $6,000. Availability is four weeks ARO.

About DDR333 Memory
The DDR333 specification is a new and faster speed grade of the industry standard DDR SDRAM family. Developed in response to the industry demand for ever-increasing system performance, DDR333 transfers data on both rising and falling edges of a 167 MHz clock for a per-pin throughput of 333 Mb per second. DDR333 chips are used on PC2700 memory modules that provide a peak throughput of 2.7 GB per second on 64 or 72 bit buses.

About FuturePlus Systems Corporation
FuturePlus Systems Corporation is a privately held manufacturer of measurement and debugging tools for the computer industry. Founded in 1991, FuturePlus Systems Corporation is a Premier Solution Partner in Agilent Technologies' Value-Added Business program. Agilent is a designated official distributor of FuturePlus Systems products. More information about FuturePlus Systems Corporation may be found on the web at www.futureplus.com.

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