New DDR4 SODIMM Interposer for Keysight Logic Analyzers

March 3, 2020 Bedford, NH: FuturePlus Systems announced today the availability of a new DDR4 SODIMM interposer exclusively for Keysight's U4164A Keysight's Logic Analyzer Module. The new FS2521 interposer is based on the U4164A's Quad Sample Mode and allows the U4164A to acquire DDR4 traffic from an SODIMM with a single probe point. The FS2521 teamed with the Keysight U4164A gives engineers 100+ channels of 10GHz timing analysis along with State analysis of Read & Write DDR4 data signals. This product brings to SODIMM implementations what the FS2520 brought to DIMM implementations back in 2016. The FS2521 replaces the FS2512 which used 2 probe points to acquire the DDR DDR4 data from the SODIMM slot.

Keysight Technologies and FuturePlus Systems are leaders in DDR Memory validation, working together for over 20 years on high speed instrumentation and debug tools for the computer industry. The team currently has the ONLY full DDR4 Validation solution on the market. In addition, Keysight and FuturePlus have the only DDR5 Logic Analyzer and interposer solution on the market.

About FuturePlus Systems: FuturePlus Systems designs, manufactures and markets tools for the computer design industry. Our products help design and test engineers ensure that their products conform to industry standards. In particular FuturePlus Systems focuses on computer bus standards such as DDR Memory, Low Power DDR Memory, DisplayPort and others. Learn More

FuturePlus Systems is a member of Keysight Technologies' Solutions Partners Program.

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