DisplayPort Validation Tools now Customizable

September 16 , 2020, Bedford, NH: Today FuturePlus System announced Check**DP**spec ™, an enhancement to its DisplayPort Protocol Analyzer tool set. "Our FS4500 DisplayPort tool has been a favorite for developers because it can sit between any source and any sink. With this announcement we have added a software coding environment that can search the acquired trace for any protocol issue or events of interest." Stated company Vice President, Barbara Aichinger.

This product is a software AddIn to the FS4500 Probe Manager that runs on the laptop or PC that the FS4500 is connected to. It will search the acquired DisplayPort data on the high-speed Main Link and look for various protocol violations, performance issues or any other event that the user is interested in.

The Check**DP**spec™ software will have an innovative feature. It will allow for customer developed tests via an AddIn that will allow customers to write and add in their own tests. Customers tests can be developed in .net /C# and will take the form of a .dll. The .dll file will appear in the list on the Validation tab screen along with the tests provided by FuturePlus and can be selected or deselected. The tests can cover Framing, Audio, Video, FES, DSC and eDP. The software will search through the acquired data and flag all of the found violations. Markers will be placed at each violation or event so that the user can correlate the error to the actual acquired data in the FS4500 Probe Manager Listing screen. A report can be generated that includes the configuration, the test results and any notes the user wishes to add.

The FS4520 Check**DP**spec™ data sheet can be found [here.](#)

About FuturePlus Systems

FuturePlus Systems designs, manufactures and markets tools for the computer design industry. Our products help design and test engineers ensure that their products conform to industry standards. In particular FuturePlus Systems focuses on computer bus standards such as DisplayPort, DDR Memory, Low Power DDR Memory and others.

Our equipment allows engineers to actually see the signals as they pass from point to point on a printed circuit board which is part of their design.   Our tools are used in various steps along the design and validation path. From early bring up to post production bug finding, FuturePlus Systems products offer our customers a useful tool that improves the quality of their product in a cost-effective manner.

In late 2016 FuturePlus Systems introduced a services branch of our company to capitalize on our vast knowledge of the bus standards that we cover. Many customers do not have the on-staff talent to debug and validate some of the complicated standards that are part of their design. FuturePlus Systems can save companies months of time and thousands of dollars by performing that testing for them using the equipment we make plus the equipment of our partners.

We have been shipping our tools since 1991. Our products are used by hundreds of different companies using standard industry computer buses in PC's, servers, mobile products and embedded computer systems around the world.

FuturePlus Systems is a member of Keysight Technologies' Solutions Partners Program.

FuturePlus Systems has technical representatives in major countries around the world.

FuturePlus® is a registered trademark of FuturePlus Systems, LLC.

DDR3 Detective® and the DDR Detective® are registered trademarks of FuturePlus Systems, LLC.

Check**DP**spec™ is a pending trademark of FuturePlus Systems, LLC.

Press Contact: Barbara Aichinger
 FuturePlus Systems, LLC.
 Barb.Aichinger@FuturePlus.com
 603-472-5905