DDR5 SideBand Bus now has a Protocol Analyzer

May 10, 2021 Bedford, NH: Today FuturePlus Systems announced the FS27x0 product line for DDR5 Developers.

The FS27x0 Product line consists of three different products:

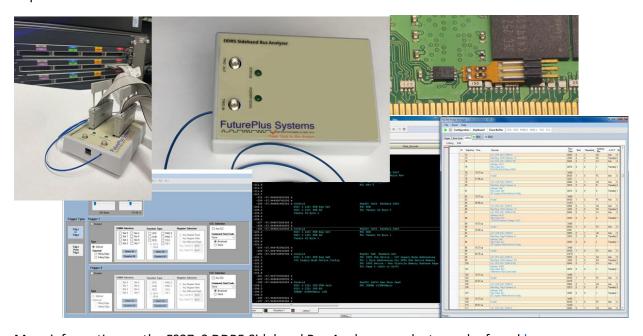
- FS2700 DDR5 SideBand Bus Protocol Analyzer
- FS2710 DDR5 SideBand Bus Preprocessor for Keysight Logic Analyzers
- FS2520 DDR5 Preprocessor and Protocol Analyzer.

The FS2520 is a combination of the FS2700 and the FS2710.

Although the DDR5 Sideband Bus is an I2C and I3C physical layer on the logical layer it covers 8 different JEDEC specifications.

- JESD403-1 JEDEC Module Sideband Bus Standard
- JESD300-5 Hub and Serial Presence Detect Device Standard
- JESD301-1 PMIC50x0 Power Management Specification (RDIMM, LRDIMM, NVDIMM)
- JESD301-2 PMIC50x0 Power Management Specification (UDIMM, SODIMM)
- JESD302-1 Serial Bus Thermal Sensor Device Specification
- JESD82-511 DDR5RCD01 Registered Clock Driver
- JESD400-5 DDR5 SPD Contents Specification
- MIPI I3C Basic Specification

The FS27x0 software decodes all of the register addresses and all the bits within each register for all the devices on a DDR5 RDIMM, UDIMM, LRDIMM and SODIMM. DDR5 System Engineers do not need to fumble through 8 different specifications and thousands of register bit definitions in order to figure out what the traffic on the DDR5 SideBand Bus is. This tool will tell them! With these tools Engineers will be able to trigger on any register access, see only events of interest with store qualification, and with the unique 'Dashboard' feature will be able to see all register accesses that have occurred on the DDR5 Sideband bus since the system was booted. Similar tools on scopes and logic analyzers have limited memory and show only the 2 wire SDA/SCL serial bus signals and do not parallelize the data like the FS27x0 line of products does.



More information on the FS27x0 DDR5 Sideband Bus Analyzer products can be found here.

FuturePlus Systems is a leading DDR5 Debug and Validation Tool vendor. Visit us at www.FuturePlus.com

About FuturePlus Systems

FuturePlus Systems designs, manufactures and markets tools for the computer design industry. Our products help design and test engineers ensure that their products conform to industry standards. In particular FuturePlus Systems focuses on computer bus standards such as DisplayPort, DDR Memory, Low Power DDR Memory and others.

Our equipment allows engineers to actually see the signals as they pass from point to point on a printed circuit board which is part of their design. Our tools are used in various steps along the design and validation path. From early bring up to post production bug finding, FuturePlus Systems products offer our customers a useful tool that improves the quality of their product in a cost-effective manner.

In late 2016 FuturePlus Systems introduced a services branch of our company to capitalize on our vast knowledge of the bus standards that we cover. Many customers do not have the on-staff talent to debug and validate some of the complicated standards that are part of their design. FuturePlus Systems can save companies months of time and thousands of dollars by performing that testing for them using the equipment we make plus the equipment of our partners.

We have been shipping our tools since 1991. Our products are used by hundreds of different companies using standard industry computer buses in PC's, servers, mobile products and embedded computer systems around the world.

FuturePlus Systems is a member of Keysight Technologies' Solutions Partners Program.

FuturePlus Systems has technical representatives in major countries around the world.

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