FOR IMMEDIATE RELEASE

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FuturePlus Systems Announces FS2910 LPDDR5/X CAMM2 Interposer for Keysight U4164A Logic Analyzer

Merrimack, NH — FuturePlus Systems is proud to announce the release of its latest innovation in DDR5 test and debug solutions: the FS2910 LPDDR5/X CAMM2 Interposer, designed exclusively for use with the Keysight U4164A Logic Analyzer. This new interposer empowers system designers and validation engineers with unprecedented visibility into the LPDDR5/X Command and Address (CA) bus—critical for debugging and optimizing LPCAMM2-based memory systems running at up to 8533 MT/s*.

With full support for 8 CA buses and 112 total signals, the FS2910 interposer offers a quick and easy mechanical, electrical, and software interface between the U4164A modules and LPCAMM2 memory connectors, enabling complete and accurate state analysis on all CA buses.

Key Features:

• Supports LPDDR5/X CAMM2 modules at up to 8533 MT/s*
• 8 CA buses with 1 U4164A logic analyzer card per bus
• Compatible with JEDEC LPDDR5/X CAMM2 specifications
• Embedded tip resistor for optimal high-frequency probing
• Buried isolation resistor improves signal integrity and eye width
• Seamless integration with Keysight B4661A DDR5 Analysis Software
• 5 GHz timing analysis capability on CA buses
• Configuration files provided for easy setup and include Keysight Eyescan

"The FS2910 interposer continues our mission to help customers design tomorrow's computers today," said Slobodan Mrdjan, CTO of FuturePlus. "With LPCAMM2 emerging as the next-generation memory form factor in client computing, we're proud to offer the industry a robust, field-ready tool that addresses critical debug and validation challenges for LPDDR5/X CAMM2 designs."

Ordering Information:

• Product Name: FS2910 LPDDR5/X CAMM2 Interposer
• Availability: Shipping now
• Software Required: Keysight B4661A-5FP/-5TP/-5NP DDR5 Analysis SW
• Hardware Required: U4164A Logic Analyzer, M9505A Chassis (1 per CA bus)

For more information, visit www.FuturePlus.com or contact Protocol.Decode@FuturePlus.com .

About FuturePlus Systems

FuturePlus Systems is the technology leader in advanced bus protocol analysis tools for the computer design industry. With decades of experience in memory and high-speed I/O debug, we help engineers tackle the toughest validation challenges across DDR, LPDDR, DisplayPort, eDP, and more.