

FOR IMMEDIATE RELEASE

FuturePlus Systems Announces FS2915 LPDDR5/X SOCAMM2 Interposer; Orders Underway with Leading Computer Industry Customers

Merrimack, NH — December 15, 2025 — FuturePlus Systems, LLC, a technology leader in high-performance protocol analysis tools for advanced computer systems, today announced the **FS2915 LPDDR5/X SOCAMM2 Interposer**, a new solution designed to deliver complete command and address (CA) bus visibility for next-generation LPDDR5/X SOCAMM2 memory designs.

FuturePlus Systems has received **multiple orders from major companies in the computer industry**, with **customer shipments scheduled to begin in Summer 2026**.

The **FS2915 LPDDR5/X SOCAMM2 Interposer** provides a mechanical, electrical, and software interface between LPDDR5/X SOCAMM2 memory modules and the **Keysight U4164A logic analyzer**, enabling accurate capture and analysis of all LPDDR5/X CA bus activity at data rates up to **8533 MT/s**. The solution supports **eight independent CA buses**, totaling **112 probed signals**, and is designed to be fully compatible with the **JEDEC LPDDR5/X SOCAMM2 specification**.

FuturePlus Systems is a **Keysight Solutions Partner**, and the FS2915 is designed exclusively for use with **Keysight M9505A chassis** and **U4164A logic analyzer modules**, requiring one logic analyzer module per CA bus. In addition to direct sales, FuturePlus products are available through **authorized resellers worldwide**, supporting customers across North America, Europe, and Asia.

“LPDDR5/X SOCAMM2 introduces new challenges for command and address visibility at very high speeds,” said **Barbara P. Aichinger, Vice President, New Business Development** at FuturePlus Systems. “The FS2915 provides engineers with the signal integrity, timing accuracy, and analysis depth required to confidently design and debug next-generation memory subsystems.”

Key features of the FS2915 include:

- Support for **eight LPDDR5/X command and address buses**
- **112 total signals** across CA, clock, reset, and sideband signals
- Compatibility with **Keysight Eyescan** and DDR5 analysis software
- Up to **12 GHz sampling rate** and **5 GHz timing analysis** on the CA bus
- Quick setup using provided Keysight configuration files

Initial customer shipments are planned for Summer 2026, with order fulfillment continuing throughout **Q3 2026**.

For more information about the FS2915 LPDDR5/X SOCAMM2 Interposer, visit www.futureplus.com.

About FuturePlus Systems

FuturePlus Systems, LLC is a leading provider of advanced protocol analysis solutions for the computer design industry. A **Keysight Solutions Partner**, FuturePlus delivers interposers, protocol analysis tools, and software that enable engineers to monitor, analyze, and verify complex high-speed bus activity across today's most advanced memory and interface technologies. FuturePlus products are sold worldwide through a network of authorized resellers.

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