

Futureplus Systems Introduces USB 2.0 Analysis Probe

COLORADO SPRINGS, CO. June 1, 2001 -- FuturePlus Systems Corporation today introduced the FS4120 Universal Serial Bus (USB) analysis probe. The new probe is designed for use with Agilent Technologies logic analyzers. Targeted users are engineers involved with debugging, testing, verification, and compliance testing of USB 2.0 peripherals and USB-based systems.

The FS4120 probe provides an electrical and mechanical interface for passive bus analysis between the USB device-under-test and Agilent logic analyzers. It has test points for DC current, inrush current and suspend current measurements for self-powered devices. SMA connectors allow easy oscilloscope triggering and measurements. By integrating a variety of connectors, FuturePlus Systems reduces the need for an external breakout board.

Key features of the new probe include:

- complete USB serial-to-parallel decode providing a protocol level view of bus traffic;
- dual-channel operation with time-correlated displays for hub analysis;
- support for current measurements, including inrush current; support for both USB 1.1 and 2.0 users;
- external power supply connection for USB devices;
- detects errors such as bad PID, invalid PID, serial bit stuffed, CRC, Start of Frame and USB reset;
- operates at all USB speeds, 480 MB/s, 12 MB/s or 1.5 MB/s;
- supports all types of data transfers, including isochronous transfers;
- LEDs that provide quick visual identification of bus status and activity; and
- SMA connections for easy triggering and connection to a high-speed oscilloscope.

Dual-port Operation Allows Simultaneous Measurements of Two Devices

The FS4120 has two separate measurement ports. This feature enables the designer to independently measure a high-speed device and a full-speed device at the same time, while having to purchase only one analysis probe. FuturePlus Systems knows of no other product that incorporates this capability.

Included Software Provides Protocol-sensitive Logic and Inverse Assembler

The FS4120 product comes with software that contains complete configuration files and a USB-protocol inverse assembler for Agilent logic analyzers. FuturePlus's expertise in transaction and protocol decoding simplifies triggering, store qualification and performance monitoring of the USB bus.

All USB cycles and transaction identifiers are decoded by protocol-sensitive clocking logic and presented to the logic analyzer as separate data bits. These packet identifiers enable users to store all USB traffic, to store only certain packet types, or to store only packets to and from a user-specified function. The FuturePlus transaction inverse assembler simplifies accurate analysis of the captured USB traffic.

Complete USB performance Monitoring

The FS4120, combined with Agilent performance-analysis software, enhanced triggering and cross-domain analysis gives users complete system and USB performance monitoring. Triggering can occur on any address, address and end point, data pattern, CRC error or USB error. Store qualifiers allow the storage of any combination of those criteria.

Price and Availability

The FS4120 USB 2.0 analysis probe has a U.S. list price of \$7995. It is available from FuturePlus Systems Corporation or from Agilent Technologies.

About FuturePlus Systems Corporation

FuturePlus Systems Corporation is a privately held manufacturer of measurement and debugging tools for the computer industry. The company's products are used in more than 20 different countries. FuturePlus Systems Corporation is a Premier Solution Partner in Agilent Technologies' Channel Partners Value-Added Business program. Agilent is a designated official distributor of FuturePlus Systems products. More information about FuturePlus Systems Corporation may be found on the web at www.futureplus.com.

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