

FS2354 DDR3 1333 SO-DIMM Interposer  
for Agilent Logic Analyzers

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## ***Product News***

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### **FuturePlus Systems Introduces New DDR 3 Memory Bus Interposer**

#### **Reliable DDR3 1333 data capture for Agilent logic analyzers**

**COLORADO SPRINGS , Colo .** , June 11, 2009 -- FuturePlus Systems Corp. today introduced the FS2354 DDR 3 1333 SO-DIMM interposer for next-generation double-data-rate ( DDR ) SDRAM buses. The new product will be used by designers involved with SO-DIMM validation, failure analysis and bus functional-parametric validation in PC gaming, servers, super computing, high-definition television, laptops, desktops and mobile computing applications.

The new interposer is designed for use with the Agilent 16900-series logic analyzer to provide state analysis and protocol decode at up to 1333 megatransfers per second (MT/s). The FS2354 is the *only* DDR 3 interposer proven to provide reliable 1333 MT/s SO-DIMM data capture data capture using an Agilent logic analyzer.

"We are always improving our interposer technology and the FS2354, combined with the new Agilent 16962A module, provides compelling insight into DDR 3 bus activity," said Edward Aichinger, President of FuturePlus Systems.

#### **Interposer Design Provides Flexibility and Ease of Use.**

The FS2354 interposer offers flexibility and out-of-box ease-of-use. The low-profile interposer design minimizes probe-induced effects on the DDR 3 bus. The interposer is inserted into the target memory socket, while also accepting a memory module in the probe's extender slot. This capability is essential when the target system has only one memory slot, or when a user must probe a fully-populated memory bus.

#### **Software Components Speed Setup and Simplify Analysis.**

The FS2354 includes software that helps users to comprehend acquired data and enhance usability. Protocol-decode software interprets bus data and displays it in protocol, hexadecimal, or binary format, and auto-configuration files facilitate quick logic analyzer setup.

#### **Supported SO-DIMMs**

The FS2354 supports a 204-pin, 667 MHz clock (1333 MT/s data rate), 64-bit, Un-buffered

Synchronous Double Date Rate 3 DRAM Dual In-Line Small Outline Memory Module (DDR 3 SDRAM SO-DIMM).

**U.S. Pricing and Availability**

The FS2354 has a U.S. list price of \$40,000. Delivery is eight weeks ARO .

**About DDR 3 Memory**

DDR 3 is the newest generation of double-data-rate memory technology. Designed to meet the increasing demand for greater performance in such applications as PC gaming, servers, super-computing, notebooks and high-definition television, its main advantages are its higher data rate and lower power consumption than earlier DDR memory. At DDR 3's top speed, 70 copies of Tolstoy's "War and Peace" could be transferred in about one second.

**About FuturePlus Systems**

FuturePlus Systems Corporation is a privately held manufacturer of bus protocol analysis tools for the computer industry. The company is a member of Agilent's Channel Partner program. FuturePlus Systems products are also supported by authorized representatives in Canada , China , Taiwan , Japan , India , most European countries, and Korea . More information about FuturePlus Systems Corporation may be found on the internet at [www.futureplus.com](http://www.futureplus.com).

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