

FS2352 DDR3 1866 DIMM Interposer
For Use With Agilent Logic Analyzers

Product News

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FS2352

DDR3 1866 DIMM
Interposer for Agilent
Logic Analyzers

Agilent Technologies, FuturePlus Introduce Industry's First DDR 3 1866 Memory Bus Debug Solution

SANTA CLARA, Calif., and BEDFORD, N.H., Aug. 10, 2009 -- Agilent Technologies Inc. and FuturePlus Systems Corp. today introduced the industry's first DDR 3 1866 DIMM interposer test solution, which is comprised of the [Agilent 16962A logic analyzer module](#) and the [FS2352 interposer](#) for next-generation double-data-rate (DDR) SDRAM buses. The new DDR 3 1866 DIMM interposer probing solution is the ideal tool for designers performing DIMM validation, failure analysis, and bus functional-parametric validation in servers, supercomputing, desktops and computing applications.

The new interposer is designed for use with the Agilent 16900 Series logic analyzer to provide state analysis and protocol decode at up to 1866 mega transfers per second (MT/s). The FS2352 is the only DDR 3 interposer proven to provide reliable 1866 MT/s DIMM data capture using an Agilent logic analyzer.

The industry's first and most comprehensive DDR 3 test platform offers the industry's fastest full-channel 2.0GT/s [16962A logic analysis module](#) , a complete probing portfolio for DDR 3 BGA and DIMM, and the first DDR 3 compliance and performance software environment.

Agilent's [16962A logic analysis module](#) with 2.0GT/s state speed and 2GHz trigger sequence speed enables full capability to reliably trigger and capture DDR 3 1866 signals. When used with the new DDR 3 probing solution and analysis software tool, this module provides full test capability for system integration in the memory industry.

“The new Agilent 16962A module, when used with the FuturePlus FS2352 Interposer, provides superior insight into DDR 3 bus activity,” said Jun Chie, marketing manager for Logic and Protocol Test at Agilent. “This solution setup is more proof of Agilent's commitment to provide designers of DDR devices with the ideal toolset to address their test

challenges.”

“We are always improving our interposer technology, and the FS2352, combined with the Agilent’s 16962A module, provides compelling insight into DDR 3 bus activity,” said Edward Aichinger, president of FuturePlus Systems.

Benefits of the 16962A logic analyzer module and the FuturePlus FS2352 solution setup include:

- Interposer design provides flexibility and ease of use: The low-profile interposer design minimizes probe-induced effects on the DDR 3 bus. The interposer is inserted into the target memory socket, while also accepting a memory module in the probe’s extender slot. This capability is essential when the target system has only one memory slot, or when a user must probe a fully populated memory bus.
- Software components speed setup and simplify analysis: The solution includes software that helps users to comprehend acquired data and enhance usability. Protocol-decode software interprets bus data and displays it in protocol, hexadecimal or binary format, and auto-configuration files facilitate quick logic analyzer setup.
- Solution supports DIMMs : The solution supports a 240-pin, 933 MHz clock (1866 MT/s data rate), 64-bit, Un-buffered Synchronous Double Date Rate 3 DRAM Dual In-Line Memory Module (DDR 3 SDRAM DIMM).

Additional information about Agilent’s 16962A logic analyzer module is available at www.agilent.com/find/16962A. FuturePlus’s FS2352 interposer is available at www.futureplus.com/products/fs2352. High-resolution images are available at www.agilent.com/find/ddr3bga_images.

U.S. Pricing and Availability

The Agilent 16962A is available now at a price of \$38,135.

The FS2352 is available now for \$40,000.

A backgrounder about Memory technology and Agilent’s portfolio is available at

www.agilent.com/find/memory_backgrounder.

More information about DDR and Agilent’s solutions is available at

www.agilent.com/find/ddr.

Agilent’s Digital Test Standards Program

Agilent’s solutions for digital applications are driven and supported by Agilent experts who are involved in various international standard committees. We call it the Agilent Digital Test Standards Program. Our experts are active in the [Joint Electronic Devices Engineering Council](#) (JEDEC), PCI Special Interest Group ([PCI-SIG](#) ®), [Video Electronics Standards Association](#) (VESA), [Serial ATA International Organization](#) (SATA-IO), [USB-Implementers Forum](#) (USB-IF), [Mobile Industry Processor Interface](#) (MIPI) Alliance, and

many others. Our involvement in these standards groups and their related workshops, plugfests, and seminars enables Agilent to bring the right solutions to the market when our customers need them.

About FuturePlus Systems

FuturePlus Systems Corp. is a privately held manufacturer of bus protocol analysis tools for the computer industry. The company is a member of Agilent's Channel Partner program. FuturePlus Systems products are also supported by authorized representatives in Canada , China , Taiwan , Japan , India , most European countries and Korea . More information about FuturePlus Systems Corp. may be found at www.futureplus.com.

About Agilent Technologies

Agilent Technologies Inc. (NYSE: A) is the world's premier measurement company and a technology leader in communications, electronics, life sciences and chemical analysis. The company's 19,000 employees serve customers in more than 110 countries. Agilent had net revenues of \$5.8 billion in fiscal 2008. Information about Agilent is available on the Web at www.agilent.com.

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