

**Agilent Technologies' Industry-First  
DDR2 Interposer Analysis Probe Enables Design  
of High-Speed Memory Buses to 533 MT/s**

**Support for All DIMM Types, Low-Profile Design  
Provide Maximum Flexibility, Ease of Use for DIMM Validation**

**PALO ALTO, Calif.**--Jan. 6, 2004-- Agilent Technologies Inc. (NYSE:A) today introduced the industry's first interposer analysis probe for next-generation double data rate 2 (DDR2) synchronous dynamic random access memory (SDRAM) buses. The new probe enables design and debug at up to 533 megatransfers per second (MT/s) using any Jedec standard DDR2 dual in-line memory module (DIMM).

The Agilent FSI-60075 DDR2 memory-bus analysis probe, developed in conjunction with FuturePlus Systems Corp., is flexible and easy to use, with a low-profile interposer design that minimizes impact on the DDR2 bus. This capability is essential for DIMM validation, failure analysis and bus functional parametric validation in desktop computers, high-end workstations and servers that have tight timing and mechanical layouts.

"The DDR2 memory bus, while providing a much-needed boost in speed, presents a significant challenge to designers of DDR2-based servers and desktops," said Ron Nersesian, vice president and general manager of Agilent's Design Validation Division. "Our probe builds on Agilent and FuturePlus Systems' 12-year history of driving new digital-design test equipment to provide a flexible, easily connected, high-fidelity link between the logic analyzer and the system under test."

Competing probing tools require that components such as memory chips, registers and PLL (phase-locked loops) be soldered directly onto the probe, creating a single test configuration that cannot be easily modified. This limits the ability of designers to test a system for all possible cases using many different DIMM configurations or test DIMMS that have been returned from the field for failure analysis. In addition, competing tools require a 1.5-inch horizontal keep-out area, which limits their usefulness for probing systems with increasingly smaller mechanical layouts.

The Agilent FSI-60075 interposer design provides maximum flexibility by enabling the probe to be inserted into the target memory socket, while accepting a memory module in the probe's extender DIMM slot so the probe can be used with any standard DIMM. This feature is especially useful for designs that have only one memory socket, as well as when analysis must be performed on a fully loaded memory bus. In addition, because of the straddle-mount DIMM slot and right-angle Samtec connectors, the FSI-60075 probe minimizes electrical and mechanical intrusion. Its vertical design minimizes the horizontal keep-out area, allowing DIMMS to be installed in slots adjacent to the probe. The in-line configuration of the probe and interposed DIMM keeps electrical lengths short, enabling reliable operation at

full DDR2-533 speeds.

The Agilent FSI-60075 is designed for use with the Agilent 16700-series of logic analyzers, which include a full line of timing/state analyzer, oscilloscope and pattern generator modules for debugging and validating high-speed digital designs. The wide variety of measurement and analysis capabilities offered by the 16700-series logic analyzers helps design teams in all stages of development, from basic circuit board turn-on to validating signal integrity. The modular form-factor of the 16700 series enables engineers to choose the hardware and data post-processing and protocol tools that meet current requirements, then expand those capabilities as needed.

### **Further Information**

More information on the Agilent FSI-60075 memory-bus analysis probe is available at [www.agilent.com/find/ddrmemory](http://www.agilent.com/find/ddrmemory).

More information on the Agilent 16700-series of logic analyzers is available at [www.agilent.com/find/logic](http://www.agilent.com/find/logic).

High-resolution images of the Agilent FSI-60075 memory-bus analysis probe are available at [www.agilent.com/find/DDR2\\_images](http://www.agilent.com/find/DDR2_images).

### **U.S. Pricing and Availability**

The Agilent FSI-60075 DDR2 400/533 SDRAM basic analysis probe is available to order now, and is priced at \$9,122.

### **About FuturePlus Systems Corporation**

FuturePlus Systems Corporation is a privately held manufacturer of measurement and debugging tools for the computer industry. The company's products are used in more than 20 different countries. FuturePlus Systems Corporation is a member of Agilent Technologies' Channel Partners Value-Added Business program. Agilent is a designated official distributor of FuturePlus Systems products. More information about FuturePlus Systems Corporation may be found on the web at [www.futureplus.com](http://www.futureplus.com).

### **About Agilent Technologies**

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