

## Agilent Technologies Announces Availability Of New DDR3 Protocol Violation Detector

### DDR3 Real-time Protocol Violation Reporting And Triggering Now A Reality

**COLORADO SPRINGS, CO**, Jan. 1, 2011 –Agilent Technologies is announcing the availability of a new DDR3 Protocol Violation Detector, called the “**DDR3 Detective**.” The **DDR3 Detective** monitors DDR3 bus traffic continuously and as long as required, while analyzing all behavior in real-time for several hundred different potential violations of the DDR3 Protocol. The **DDR3 Detective** will be available through all Agilent sales channels, and is compatible with Agilent logic analyzers and oscilloscopes.

The current methodology to test for DDR3 protocol violations involves either complex, one at a time, logic analyzer triggers or post processing software that only looks at a tiny slice of the DDR3 memories operation. The **DDR3 Detective**, manufactured by FuturePlus Systems Corporation, analyzes real time command activity that may be breaking the rules of the DDR3 specification and threatening data integrity. The problems may only occur infrequently. These defects can lead to system problems that may not be obvious until later in the product cycle, when there is a wider population of devices and configurations. Catching these problems and identifying their root cause early is the key to fixing them before silicon and/or firmware becomes widely disseminated.

“The **DDR3 Detective** line of products adds real value to our customers’ logic analyzers and high performance oscilloscopes,” said Ross Nelson, General Manager of Agilent’s Digital Debug Division. “This is the type of product that customers have been asking for. Agilent will aggressively seek out these state of the art, leading edge types of solutions and make sure they are compatible with our products and available to our customers.”

“The **DDR3 Detective** is a new, paradigm-changing product. We can now offer our customers not only a full speed trace of system activity, but intelligent and automated analysis of DDR3 protocol and performance. DDR3 developers will be able to cut time to market with this new product because they will be assured that their design meets the specification,” said Edward Aichinger, President of FuturePlus Systems.

The new **DDR3 Detective** is targeted at designers involved with DDR3 DIMM or SO-DIMM memory system failure analysis and bus functional-parametric validation in PC gaming, servers, super computing, high-definition television, laptops, desktops and mobile computing applications.

#### **Interposer Design Provides Flexibility and Ease of Use.**

The **DDR3 Detective** is bundled with several types of interposers to connect to the system under test.

The **FS2444** and **FS2445** are bundles that connect to the Address, Command and Control signals of the DDR3 DIMM or SO-DIMM bus.

The **FS2441** and **FS2443** are bundles that connect to the Address, Command, Control and DQ signals of the DDR3 DIMM or SO-DIMM bus.

The product line also includes support for probing a second slot on a single channel. This gives added coverage for designs that have two DDR3 slots on a single DDR3 channel. This is another new innovation from FuturePlus Systems

Further, the **DDR3 Detective** can connect to an Agilent logic analyzer or oscilloscope either individually or simultaneously.

The FS2444 and FS2445 bundles include the basic DDR3 Detective plus the FS2433 or FS2434 interposers. These bundles are priced at \$40,000 (USA only).

The FS2441 and FS2443 bundles include the basic DDR3 Detective plus the FS2435 or FS2436 interposers. These bundles are priced at \$60,000 (USA only).

#### **Supported DIMMs and SO-DIMMs.**

The FS2433 and FS2435 interposers support a 240-pin, 933 MHz clock (1867 MT/s data rate), 64-bit, UDIMM, LRDIMM and RDIMM Synchronous Double Data Rate 3 DRAM Dual In-Line Memory Module (DDR3 SDRAM DIMM.)

The FS2434 and FS2436 interposers support a 204-pin, 800 MHz clock (1600 MT/s data rate), 64-bit, Un-buffered Synchronous Double Data Rate 3 DRAM Small Outline Dual In-Line Memory Module (DDR3 SDRAM SO-DIMM).

#### **Software Interface Speeds Setup and Simplifies Analysis.**

The **DDR3 Detective** includes a PC-based Probe Manager interface that allows the user to set up the DDR3 Detective, control which tests are being made, and report the violations and performance metrics. The DDR3 Detective is controlled via a USB interface between the PC and the DDR3 Detective.

#### **Availability**

All DDR3 Detective products are available 8-10 weeks ARO.

#### **More Information**

**DDR3 Detective:** <http://www.futureplus.com/DDR3-Detective-Agilent/summary.html>

#### **About Agilent Technologies**

Agilent Technologies Inc. (NYSE: A) is the world's premier measurement company and a technology leader in chemical analysis, life sciences, electronics and communications. The company's 18,500 employees serve customers in more than 100 countries. Agilent had net revenues of \$4.5 billion in fiscal 2009. Information about Agilent is available at [www.agilent.com](http://www.agilent.com).

#### **About FuturePlus Systems**

FuturePlus Systems Corporation is a privately held manufacturer of bus protocol analysis tools for the computer industry. The company is an Agilent Solutions Partner. FuturePlus Systems products are also supported by authorized resellers in Canada, China, Taiwan, Japan, India, most European countries, and Korea. More information about FuturePlus Systems Corporation may be found on the internet at [www.futureplus.com](http://www.futureplus.com).