



IEEE1394 Analysis Probe FS4200

Quick Start Instructions

Step 1. After removing the probe tip assemblies from your logic analyzer cables, plug the logic analyzer cables into the 40 pin cable headers on the FS4200 module. The tables below give the correspondence between the FS4200 cable headers and the logic analyzer cables.

For State Analysis Only

<u>FS4200</u>	<u>16550 & 166x</u>	<u>16555/6/7 & 167x</u>	<u>1690x</u>
POD 1	Pod 1	Master Card Pod 1	Use General Purpose Probe
POD 2	Pod 2	Master Card Pod 2	
POD 3	Pod 3	Master Card Pod 3	
POD 4	Pod 4	Master Card Pod 4	

For State & Timing Analysis

<u>FS4200</u>	<u>16550 & 166x</u>	<u>16555/6/7 & 167x</u>	<u>1690x</u>
POD 1	Pod 1	Master Card Pod 1	Use General Purpose Probe
POD 2	Pod 2	Master Card Pod 2	
POD 3	Pod 3	Expander Card Pod 1	
POD 4	Pod 4	Expander Card Pod 2	
POD 5	Pod 5	Master Card Pod 3	
POD 6	Pod 6	Master Card Pod 4	

If State & Timing Analysis are required using 2 separate 16555/6/7 cards then connect the analyzer for state as in the first table and connect Pod 1 and Pod 2 of the analyzer for timing to POD 1 and POD 2 (respectively) on the FS4200.

Step 2. Power on the logic analyzer.

Step 3. Connect the FS4200 module into the IEEE1394 Serial Bus using one of the three IEEE1394 connectors on the analyzer. If the FS4200 is not connected as a “leaf node” use the remaining two IEEE1394 ports to connect to other IEEE1394 nodes.

Cont'd on the reverse side.

Step 4. 16500, 166x and 167x users load the logic analyzer with the appropriate file (see table below) from the 16500 FS4200 software diskette.

16600/ 16700 users install the IEEE1394 Analysis Probe software from the diskette labeled *16600/ 16700 Analysis Probe Install Disk for the FS4200*. Insert the flexible diskette into the Logic Analysis System diskette drive and select the **Software Install** option on the **System Administration Tools** menu. Select Flexible Disk as the media, click Apply and then select the FS4200 package and click Install (this will install the FS4200 software on the Logic Analysis System and only needs to be done once). Use the Setup Assistant to load the appropriate configuration by selecting FuturePlus -> IEEE1394 or load the logic analyzer with the required configuration file from the configs/FuturePlus/FS4200 directory (see table below).

Logic Analyzer Card	Config File	Comment
166x	CFW42_1	State Analysis only. 4 pods required.
166x	CFW42_2	State & Timing Analysis. 6 pods required.
16550 & 16710/1/2	CFW42_3	State Analysis only. 1 card, 4 pods required
16550 & 16710/1/2	CFW42_3	State & Timing Analysis. 1 card, 6 pods required
16555/6/7, 167x	CFW42_4	State Analysis only. 1 card, 4 pods required.
16555/6/7	CFW42_4 & CFW42_5	State & Timing Analysis. 2 cards, 6 pods required. Cards configured as 2 separate logic analyzers.
16555/6/7	CFW42_6	State & Timing Analysis. 2 cards, 6 pods required. Cards configured as one logic analyzer (expander below master)
16715/6/7	CFW42_7	State Analysis only. 1 card, 4 pods required.
16715/6/7	CFW42_7 & CFW42_8	State & Timing Analysis. 2 cards, 6 pods required. Cards configured as 2 separate logic analyzers.
16715/6/7 or 16750/1/2	CFW42_9	State & Timing Analysis. 2 cards, 6 pods required. Cards configured as one logic analyzer (expander below master)

1690x Analyzer users - The CD contains a setup file that will automatically install the configuration files and protocol decoder onto a PC containing the 16900 operating system or onto a 16900 analyzer itself. To install the software simply double click the .exe file on the CD containing the 16900 software. After accepting the license agreement the software should install within a couple of minutes. The following configuration files will be installed.

1690x Logic Analyzer	File Name	Comments
1674x,16750-2, 1691x	CFW42_1	1 Card, state analysis
1674x,16750-2, 1691x	CFW42_2	1 Card, timing analysis
1674x,16750-2, 1691x	CFW42_3	2 Cards, 1 machine, state and timing analysis

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