



USB Analysis Probe FS4100

State Analysis

Step 1. Set the “low speed only” switch to the desired setting.

<u>Switch</u>	<u>Setting</u>	<u>LED</u>
On	Low speed only segment	ON
Off	High speed/low speed segment	OFF

Step 2. Turn off the logic analyzer. Remove the probe tip assemblies and plug the logic analyzer cables into the Analysis Probe cable headers. POD 1 to Header 1 and POD 2 to Header 2.

Step 3. Plug the USB Analysis Probe IN cable to the upstream Host or Hub. The Analysis Probe OUT connector can be connected down stream to either a Hub or device.

Step 4. Turn on the logic analyzer. The VCC and READY LEDs should light. NOTE: Connect the USB Analysis Probe to the USB before powering on the logic analyzer.

Step 5. 16500 users load the appropriate configuration file into the logic analyzer from the diskette labeled 16500 Analysis Probe Software for the FS4100(see table below). NOTE: Do **NOT** select SYSTEM or ALL as the target of the LOAD operation. On the 16500 systems, select the logic analyzer card. On the portables, select ANALYZER.

<u>Logic Analyzer</u>	<u>File name</u>
1660x	USB660
16550A	USB550
1655x, 16710/1/2	USB555

cont'd

Step 5 cont'd.

16700/702 users: Install the 16600/16700 USB Analysis Probe software from the diskette labeled *16600/ 16700 Analysis Probe Install Disk for the FS4100*. Insert the flexible diskette into the Logic Analysis System diskette drive and select the **Software Install** option on the **System Administration Tools** menu. Select Flexible Disk as the media, click Apply and then select the FS4100 package and click install. (This will install the FS4100 software on the Logic Analysis System and only needs to be done once). Use the Setup Assistant to load the appropriate configuration by selecting FuturePlus -> USB or load the logic analyzer with the required configuration file from the **configs/FuturePlus/FS4100** directory. (See table below)

1680/90/900 users: Insert the FS-4100 CD-ROM containing the 1680/90/900 Logic Analyzer software into the PC. Execute the .exe file to install the FS4100 software. Load the configuration file from the folder that was placed on the desktop during installation.

Configuration Files:

<u>Logic Analyzer</u>	<u>File name</u>
16550, 16710/1/2	CUSB41_1
16555/6/7	CUSB41_2
16715/6/7	CUSB41_3
1680/90/900	
16750/1/2, 1691x (State)	CUSB41_6
1680/90/900, 16750/1/2, 1691x (Timing)	CUSB41_7

You're ready to GO!

Timing Analysis

Step 1. Follow the steps above but connect POD 3 to Header 3 of the USB Analysis Probe. See the *USB Analysis Probe users manual* for a description of the signals on POD 3.

Step 2. Use Trigger2, Waveform2 and arming control to control the timing analyzer.

You're ready to GO!

**FuturePlus Systems Corporation Technical Support 603-471-2734 - Sales support 719-278-3540
Please check our website for more information www.futureplus.com**