



EIO PCI Analysis Probe FS3030

Quick Start Instructions

State Analysis

Step 1. Connecting the Logic Analyzer: After removing the probe tip assemblies, plug the logic analyzer cables into the Analysis Probe module cable headers as shown in the following table.

<u>Logic Analyzer</u>	<u>PCI Analysis Probe</u>	<u>Comment</u>
POD 1	Header 1	
POD 2	Header 2	
POD 3	Header 3	L clock
POD 4	Header 4	

Step 2. Install into the Target: Install the PCI EIO Analysis Probe module into a slot in the target PCI Local Bus.

Step 3. Setting up the 16600/16700 Analyzer

The 16600/16700 requires a special install procedure to install the PCI Inverse Assembler. To accomplish this, insert the diskette labeled PCI INVERSE ASSEMBLER INSTALLATION DISK FOR THE 16600/16700 into the 16600/16700 diskette drive. From the SYSTEM ADMINISTRATION TOOLS select *INSTALL* under *SOFTWARE*. From the SOFTWARE INSTALL screen select the *FLEXIBLE DISK* and *APPLY*. Once the title appears select it and then select *INSTALL*. **This procedure does not need to be repeated. It only needs to be done the first time the PCI Probe Adapter is used.** When this has completed, load the appropriate configuration file as per the instructions in the manual.

Step 4. Install the Logic Analyzer Software Load the logic analyzer with the appropriate file (*see table below*) from the FS3030 Analysis Probe diskette. When loading the file be sure to select the Logic Analyzer as the target of the load (DO NOT SELECT ALL OR SYSTEM)

<u>Logic Analyzer</u>	<u>State Analysis</u>	<u>Timing Analysis</u>
166x	F660PE	F660PE
16555,\ 167x	F555PE	F555PE
16550	F550PE	F550PE

You're ready to GO!

Timing Analysis with the logic analyzer

Follow steps 1, 2, 3 and 4 above.

In the logic analyzer configuration menu change the type field from STATE to TIMING.

You're ready to GO!

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