

# FBDIMM Interposer Probe – FS2343

## Quick Start Instructions

1. Logic Analyzer – It is recommended that 16753/4/5/6 or 16950 cards be used because of their superior setup/hold performance, signal loading characteristics, and ability to convey SM bus signals through their Pod connections. The 169xx analyzer is required and it should have A.03.00.00 system software or higher.

2. Software – There is a CD that contains the configuration files and the Protocol Decoder. Install this CD and simply click on the file FBDIMM.exe. This will load all the files onto the logic analyzer. After the software is licensed you should be ready to select an appropriate configuration file. Follow the instructions in the User Manual and on the Software Licensing Certificate.

Next load the Probe Control software, which is the FBD Probe Control.exe file. This software will load on to the analyzer and be available under the Setup tab of the analyzer workspace as the FBDIMM Probe. When using the Probe Control software always press the “Apply” button after making any changes.

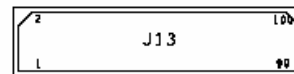
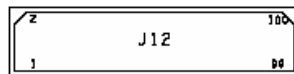
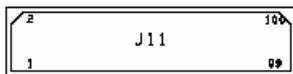
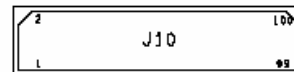
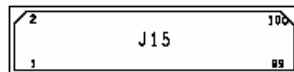
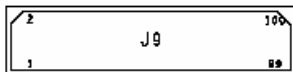
3. Logic Analyzer cards – The logic analyzer cards need to be combined into one analyzer machine of either 3,4 or 5 cards depending on the probe configuration desired. The table below details the functionality of each configuration provided. Once the configuration file is loaded onto the machine use the General Purpose Probe tool to guide you through the connections from the cards thru the adapter cables to the FS2343 Probe Paddle card.

### Configuration files – For Rev. 2 Paddle cards.

See User Manual rev. 1.2 or contact FuturePlus for Rev. 1 Paddle Card configuration file details

Probe Configuration	Configuration File	Probe Connections to the Analyzer	State Analysis requirement
SB and NB up to 533 Mb/s with 10 trigger bits	FB238_5	J10 (Master) Odd and Even, J13 Odd and Even, J9 Odd and Even, J11 Odd and Even, J12 Odd	3 cards configured as one module for speeds up to 533 Mb/s <b>Using J15 Odd requires addition of jumpers on rev. 2 Paddle cards</b>
SB Only and 8 Trigger bits	FB238_4	J9 Even, J10 (Master) Odd and Even, J12 Odd, J13 Odd and Even	3 cards configured as one module, one dual sample state machine
Full NB and SB with 5 Trigger bits	FB238_2	J9 Odd and Even, J10 (Master) Odd and Even, J11 Odd and Even, J12 Odd, J13 Odd and Even	5 cards configured as one module, one dual sample state machine
12 lane NB and SB with 10 Trigger bits	FB238_1	J9 Odd and Even, J10 (Master) Odd and Even, J11 Odd and Even, J12 Even, J15 Odd	5 cards configured as one module, one dual sample state machine <b>Using J15 Odd requires addition of jumpers on rev. 2 Paddle cards</b>

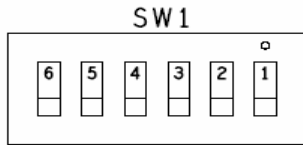
4. Probe Connections – The following figure shows the Paddle card connector locations. The General Purpose Probe feature will guide the user through the connections based on the configuration file selected.



### SM (SMBus) Control – Paddle Card settings

The FS2343 probe is designed so that AMB/LAI device control can be either from the 16900 logic analyzer and the Probe Control application software resident there, or from another FBDIMM probe (slave mode).

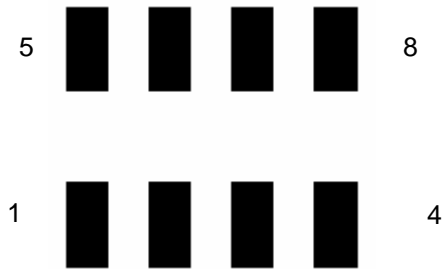
This feature is controlled by means of a 6 position switch on the probe paddle card near the logic analyzer connections.  
 The factory configuration is for 16900 control of the probe. The settings on the switch are dependent on the configuration file used. The following picture shows the jumper and switch configurations on the paddle card.



Config file	SW# ON, all others off
FB238_1	1 and 4
FB238_2	1 and 4
FB238_4	1 and 4
FB238_5	1 and 4

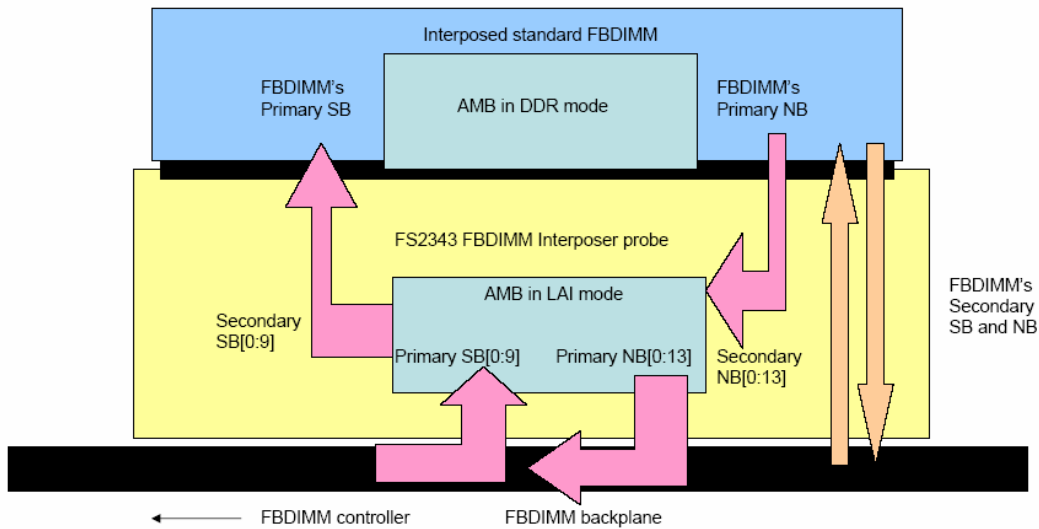
**Probe Settings**

There is a 4 position jumper location at U8 on the top side of the FS2343 Slot probe. There are jumpers that can be placed over each of the 4 set of pins. They have the following function:



Jumper	Function
1 - 5	Probe ID bit 0 Address 20
2 - 6	Probe ID bit 1 Address 21
3 - 7	Jumper removed = AMB SMBus connected to analyzer at powerup. Factory config – installed Jumper installed = AMB SMBus connected to motherboard at power up.
4 - 8	Unused

FS2343 FBDIMM Interposer Probe  
 Block Diagram



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 FS2343 QS rev. 1.3