

DDR2 SODIMM Analysis Probe – FS2337

Quick Start Instructions

1. Check Logic Analyzer – It is recommended that 16753/4/5/6 cards be used because of their superior setup/hold performance and signal loading characteristics. The logic analyzer should have operating system version A.02.80.00 or higher installed for 167xx, 169xx users need Agilent software version 2.50.00 or better.

2. Connect the probe to the logic analyzer per the following table:

*table below shows connections for 167xx frames, 169xx users, use General Purpose Probe feature in overview screen after loading the configuration file.

FS2337 Conn (J)	Analyzer Pod 2 card timing configuration	Analyzer Pod 4 card dual sample config
J3 Odd	D3(Master)	C3 (Master)
J3 Even	D4(Master)	E1
J4 Odd	D1(Master)	C1(Master)
J4 Even	D2(Master)	C2(Master)
J5 Odd	E1	D1
J5 Even	E2	D3
J6 Odd	E3	B1
J6 Even	E4	B3

Pod assignments are the same for Standard or Reverse Upper modules

3. Install the Protocol Decoder and Configuration files from the floppy using the 16700/702 software installation procedure outlined in the FS2337 manual for the 167xx. This will install a protocol decoder called "IFS2337E" in the ia directory, and will install configuration files in the /logic/configs/FuturePlus/FS2337 directory that allow you to easily configure the analyzer for timing or state operation with the FS2337. **For 169xx and offline users** insert the CD containing the windows software for the FS2337 in the CD drive of the PC or 169xx. Locate the .exe file on the CD and double click the .exe file. The .exe file will run and install the software onto the analyzer or PC. Refer to the User Manual for FS1140 and Transaction View installations if your product was provided with these tools. After successful installation there will be a shortcut on the desktop, clicking on the folder will bring you to the location of the configuration files. Double click on the correct configuration file for you setup, the configuration file and decoder should load.

Load the appropriate configuration file based on the Upper module being used

167xx Analyzer	169xx Analyzer	Timing	State
1675x, 1695x, 1691x	1675x, 1695x, 1691x	DR237_2 (STD Upper)	DR237_1 (STD Upper)
1675x, 1695x, 1691x	1675x, 1695x, 1691x	DR237_6 (RVS Upper)	DR237_5 (RVS Upper)

4.. If you are doing timing analysis you are set to capture data without further set up once the timing configuration file is loaded. If you are doing state analysis you must adjust the setup and hold settings to capture data properly. Please consult the FS2337 manual on adjusting the setup and holds properly.

5. Once the setup and holds are calibrated per the User Manual you are ready to capture data using state analysis.

Probe Setup

The 16753/4/5/6 cards require the E5378A adapter cables. 1 Adapter cable is required for 2 pods. The user has a choice of different modules depending on your target's configuration, a Reverse or Standard Upper module. Also the bottom half of the probe has 2 different Lower modules with different connectors (a 5 mm height connector and a 16 mm height connector for attaching the Upper module) for each Upper module, choose the correct Lower module to best fit your target.

The user may find it more convenient to install the Lower module into the target system, then connect the logic analyzer adapter cables to the Upper module. Finally, mate the Upper and Lower modules on the target being careful to not overstress the target's SODIMM connector during assembly.

It is not recommended to probe 2 connectors simultaneously on a single channel system.

Using EyeScan – see the User Manual for instructions.

For Technical Support call 603-471-2734
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Please visit our web site at www.futureplus.com

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