

SCSI

SCSI Analysis Probe FS2232

Supports up to 80M Transfers/sec at 8 and 16 bit

Quick Start Instructions

State Analysis

Step 1. Set the front panel switches to the desired settings.

Switch	Setting
State Parity Check	On
State Parity Check	Off
Bus Type	Single-Ended
Bus Type	Differential Bus
Bus Size	8 Bit
Bus Size	16 Bit

Note: This switch allows the user to switch off the parity check. Excessive parity errors may appear on screen if parity is on with a system that does not use parity.

Step 2. Power off the logic analyzer and remove the probe tip assemblies, plug the logic analyzer cables into the Analyzer Probe cable headers as shown in the following table. The correspondence between the SCSI Analyzer Probe headers (AP) and the logic analyzer headers (LA) is 1 to 1, 2 to 2, etc. unless otherwise stated.

Logic Analyzer	File Name	Comment
16550A, 1660x, 1661x	FSCSI2	STATE: LA Pods1,2 to AP Pods 1,2 TIMING: LA Pods 3,4 to AP Pods 3,4
1662x	GSCSI2	STATE: Pods 1-2 TIMING: Pods 3-4
1663x	GSCSI2	STATE: Pods 1-2
16554/5/6/7x,	HSCSI2	STATE: Pods 1-2 TIMING: Pods 3-4
1670x, 1671x, 1672x	HSCSI2	STATE: Pods 1-2 TIMING: Pods 3-4

Step 3. Power off the SCSI Target. Disconnect one end of a SCSI cable from the SCSI target and connect it to one of the connectors on the FS2232. Connect the optional or custom cable from the matching connector on the FS2232 to your target.

Step 4. The **16500/166x/167x** Frames require the user to load the logic analyzer with the appropriate file (See table above) from the 16500 FS2232 software diskette.

The **16700 users** install the FS2232 Analysis Probe Software from the diskette labeled 16600/16700 Analysis Probe Software for the FS2232. Insert the flexible diskette into the Logic Analysis System diskette drive and select **Software Install** option on the **System Administration Tools** menu. Select Flexible Disk as the media, click Apply and then select the FS2232 package and click Install (this will install the FS2232 software on the Logic Analysis System and only needs to be done once). Use the Setup Assistant to load the appropriate configuration by selecting FuturePlus ->SCSI -> or load the logic analyzer with the required configuration file from the configs/FuturePlus/FS2232 directory (see table below).

Logic Analyzer Card	File Name
16550	CFS32_1
16555/6/7	CFS32_2
16715/6/7	CFS32_3

Power on the SCSI target and **you're ready to Go!**

Timing Analysis

Step 1. Connect the logic analyzer cables to the appropriate header on the SCSI Analysis Probe as shown in the above table.

Step 2. Load the logic analyzer with the appropriate TIMING file as listed in the above table.

You're ready to GO!

For more detailed information refer to the FS2232 Users Manual

For Technical support: Call 603-471-2734 or e-mail to Tech_Sup@FuturePlus.com

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