

# SCSI

# SCSI Analysis Probe FS2230/FS2231

Supports up to 40M Transfers/sec at 8 and 16 bit

## Quick Start Instructions

### State Analysis

**Step 1.** Set the front panel switches to the desired settings.

<u>Switch</u>	<u>Setting</u>
State Parity Check	On
State Parity Check	Off
Bus Type	Single-Ended
Bus Type	Differential Bus
Bus Size	8 Bit
Bus Size	16 Bit

Note: This switch allows the user to switch off the parity check. Excessive parity errors may appear on screen if parity is on with a system that does not use parity.

**Step 2.** Power off the logic analyzer and remove the probe tip assemblies, plug the logic analyzer cables into the Analyzer Probe cable headers as shown in the following table. The correspondence between the SCSI Analyzer Probe headers (AP) and the logic analyzer headers (LA) is 1 to 1, 2 to 2, etc. unless otherwise stated.

<u>Logic Analyzer</u>	<u>File Name</u>	<u>Comment</u>
1650A/B, 1652B, 16510A/B 16511B	CSCSI2  DSCSI2	STATE: Pods 1-2 TIMING: Pods 3-4  STATE: LA Pods 1-2 upper card to AP Pods 1-2 TIMING: AP Pods 3-4 to LA Pods 1-2 lower card
16541/540A, D 16541/540A, D 16542A 16542A	ESCSI2ST ESCSI2TM ESCSI2ST ESCSI2TM	STATE: Master Card LA Pod 1 to AP Pod 1, Exp Card LA Pod 1 to AP Pod 2 TIMING: Exp Card LA Pod 2 to AP Pod 3, Exp Card LA Pod 3 to AP Pod 4 STATE: Exp Card LA Pod 1 to AP Pod 2, Master Card LA Pod 1 to AP Pod 1 TIMING: Exp Card LA Pod 1 to AP Pod 4, Master Card LA Pod 1 to AP Pod 3
16550A, 1660x, 1661x 1662x 1663x	FSCSI2 GSCSI2 GSCSI2	STATE: LA Pods 1,3 to AP Pods 1,2 TIMING: LA Pods 5,6 to AP Pods 3,4 STATE: Pods 1-2 TIMING: Pods 3-4 STATE: Pods 1-2
16555x, 16554x, 16556x 16717/8/9/, 16750/1/2 1670x, 1671x, 1672x	HSCSI2 HSCSI2 HSCSI2	STATE: Pods 1-2 TIMING: Pods 3-4 STATE: Pods 1-2 TIMING: Pods 3-4 STATE: Pods 1-2 TIMING: Pods 3-4

**Step 3.** Power off the SCSI Target. Disconnect one end of a SCSI cable from the SCSI target and connect it to one of the connectors on the FS2230/FS2231. Connect the optional or custom cable from the matching connector on the FS2230/FS2231 to your target.

**Step 4.** Load the logic analyzer with the appropriate file (See table above) from the FS2230/FS2231 diskette.  
Power on the SCSI target and **you're ready to GO!**

### Timing Analysis

**Step 1.** Connect the logic analyzer cables to the appropriate header on the SCSI Analysis Probe as shown in the above table.

**Step 2.** Load the logic analyzer with the appropriate TIMING file as listed in the above table.

**You're ready to GO!**

**For more detailed information refer to the FS2230/FS2231 Users Manual**

For Technical support: Call 603-471-2734 or e-mail to Tech\_Sup@FuturePlus.com

For Sales and Marketing call 719-380-7321 or e-mail to Sales@FuturePlus.com

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