



PCI-X Analysis Probe FS2104/FS2105

Quick Start Instructions

State Analysis

- Step 1.** Remove the protective cover from the BGA connector on the FS2104.
- Step 2.** Connect the FS2104 card to the E2929A using caution to line up the BGA connector.
- Step 3.** Secure the FS2104 to the E2929A using the supplied spacers, screws, and nuts.
- Step 4.** Install the assembly into a PCI-X slot in the target PCI-X system being careful not to use the top of the FS2104 to seat the assembly.
- Step 5.** Connect the E5346A cables to the Mictor connectors on the FS2104 using the labels on the shrouds for placement.
- Step 6.** Configure the cards in the logic analyzer with the Expander below the Master. Connect the cards to the FS2104 as follows:

<u>Logic Analyzer</u>		<u>FS2104</u>	<u>Logic Analyzer</u>		<u>FS2104</u>
Master	POD 1	POD 1	Expander	POD 1	POD 5
	POD 2	POD 2		POD 2	POD 6
	POD 3	POD 3		POD 3	POD 7
	POD 4	POD 4		POD 4	POD 8

- Step 7.** Install the FS2104 Software:

Insert the Protocol Error Software into the HP16700 diskette drive. From the SYSTEM ADMINISTRATION TOOLS select INSTALL under SOFTWARE. From the SOFTWARE INSTALL screen select the FLEXIBLE DISK and APPLY once the title appears select it and then select INSTALL. Repeat this procedure with the Transaction Decode Software. This process only needs to be done once. When this has completed load the configuration file CP214_1 from the/configs/FuturePlus/FS2104 directory into the targeted logic analyzer. DO NOT SELECT ALL.

Logic Analyzer Pod Pinout

This table describes physically on what channel of the logic analyzer signals appear. Bit re-ordering has been done and can be seen in the FORMAT menu.

Pod Pinout	POD 1	POD 2	POD 3	POD 4	POD 5	POD 6	POD 7	POD 8
Ch16	DBI_A_LV_5	CLK_ERROR	FRAME	PCICLK	D_FRAME	IRDY	PAR64	XACT_TRAN64
15	DBI_A_LV_4	FP_D_15	LOCK	PERR	ADD15	ADD31	ADD47	ADD63
14	DBI_A_LV_3	FP_D_14	IDSEL	SERR	ADD14	ADD30	ADD46	ADD62
13	DBI_A_LV_2	FP_D_13	PCIX_MEN	PAR	ADD13	ADD29	ADD45	ADD61
12	DBI_A_LV_1	FP_D_12	PROT_RULE_ERROR	PCIRST	ADD12	ADD28	ADD44	ADD60
11	DBI_A_LV_0	FP_D_11	CBE_0	XACT_CMD0	ADD11	ADD27	ADD43	ADD59
10		FP_D_10	CBE_1	XACT_CMD1	ADD10	ADD26	ADD42	ADD58
9		FP_D_9	CBE_2	XACT_CMD2	ADD9	ADD25	ADD41	ADD57
8		FP_D_8	CBE_3	XACT_CMD3	ADD8	ADD24	ADD40	ADD56
7		FP_D_7	CBE_4	DCOMP_ERROR	ADD7	ADD23	ADD39	ADD55
6		FP_D_6	CBE_5	SPLIT_PEND	ADD6	ADD22	ADD38	ADD54
5		FP_D_5	CBE_6	TRDY	ADD5	ADD21	ADD37	ADD53
4		FP_D_4	CBE_7	DEVSEL	ADD4	ADD20	ADD36	ADD52
3		FP_D_3	BUS_STATE3	STOP	ADD3	ADD19	ADD35	ADD51
2		FP_D_2	BUS_STATE2	TERM0	ADD2	ADD18	ADD34	ADD50
1		FP_D_1	BUS_STATE1	TERM1	ADD1	ADD17	ADD33	ADD49
0		FP_D_0	BUS_STATE0	TERM2	ADD0	ADD16	ADD32	ADD48

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