



PCI-X State Analysis Probe FS2009

Quick Start Instructions

State Analysis

Step 1. Connect the logic analysis card to the FS2009 as follows:

<u>Logic Analyzer</u>	<u>FS2009</u>	<u>Comment</u>
Pod 1	J1 Odd	J clock
Pod 2	J1 Even	
Pod 3	J2 Odd	
Pod 4	J2 Even	
Expander Pod 1	J3 Odd	64 bit only
Expander Pod 2	J3 Even	64 bit only

Please consult your Format Menu after loading a configuration file to see how the cards are configured.

Step 2. Insert the FS1022 Interposer between the target card and the systems board, or solder the backside attach board (FS1021) to the PCI-X target connector.

Step 3. Attach the cable from the FS2009 probe to the backside attach board or interposer in the target system.

Step 4. Install the FS2009 Software:

To install the FS2009 configuration file(s), insert the diskette labeled **16700/702 Installation disk for the FS2009** into the Agilent 16700 diskette drive. From the SYSTEM ADMINISTRATION TOOLS select *INSTALL* under *SOFTWARE*. From the SOFTWARE INSTALL screen select the *FLEXIBLE DISK* and *APPLY*. Once the title appears select it and then select *INSTALL*. **This one install disk will install configuration files and the inverse assembler.**

Step 5. Load the logic analyzer with the appropriate configuration file from the logic/configs/FuturePlus/FS2009 directory. See chart below for correct file to load.

Logic Analyzer	File name for State Analysis	Cards Required	Description
16715/6/7/9or 16750/1/2	CP209_1	2 Cards together	64 bit
16753/5/6	CP209_2	2 Cards together	64 bit
16715/6/7/9 or 16750/1/2	CP209_3	2 Cards together	32 bit
16753/5/6	CP209_4	1 Card	32 bit

Offline Analysis

A licensed tool is provided for off line analysis on a PC of traces captured by a 16700 series logic analyzer. See the User Manual for details.

Timing Adapter Board

Step 1. – Connect logic analyzer adapter cables to the Timing board as follows:

<u>Logic Analyzer</u>	<u>Timing Bd</u>	<u>Comment</u>
Pod 1	J2 Odd	J clock
Pod 2	J2 Even	
Pod 3	J3 Odd	
Pod 4	J3 Even	
Expander Pod 1	J4 Odd	
Expander Pod 2	J4 Even	

Please consult your Format Menu after loading a configuration file to see how the cards are configured.

Step 2. Attach the cable from the FS2009 probe to the Timing board and then attach the Timing board to the FS1022 Interposer.

Step 3. Install the FS2009 Timing adapter configuration files, insert the diskette labeled **16700/702 Installation disk for the FS2009** into the Agilent 16700 diskette drive. From the SYSTEM ADMINISTRATION TOOLS select *INSTALL* under *SOFTWARE*. From the SOFTWARE INSTALL screen select the *FLEXIBLE DISK* and *APPLY*. Once the title appears select it and then select *INSTALL*.

Step 4. Load the logic analyzer with the appropriate configuration file from the logic/configs/FuturePlus/FS2009 directory. See chart below for correct file to load.

Logic Analyzer	Config file name for Timing analysis	Description
16715/6/7/9 or 1675x	Timing_Probe_Mode_1	Timing analysis for Mode 1 (single data rate only)
16715/6/7/9 or 1675x	Timing_Probe_Mode_1_Eyefinder	
16753/4/5	Timing_Probe_Mode_1_Eyescan	
16753/4/5/6	Timing_Probe_Mode_2	Timing analysis for Mode (double data rate only)
16753/4/5/6	Timing_Probe_Mode_2_Eyefinder	Exclusively DDR traffic
16753/4/5/6	Timing_Probe_Mode_2_Eyescan	Exclusively DDR traffic

Please note that an advanced logic analyzer user can make the changes that are represented by these different configuration files by going into the analyzer workspace and making the appropriate modifications on the Format and Sampling windows.

It is recommended that whenever possible the 16753/4/5/6 cards are used because they offer significantly less loading to the bus and also offer the ability to set the CLK threshold at a different level than the data signals.

In order for useful Eyefinder and Eyescan measurements to be made in Mode2 there has to be ONLY DDR data on the bus during the measurement.

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