



PCI Analysis Probe and Extender card FS2005 (3.3v) and FS2006 (5v)

Quick Start Instructions

State Analysis

Step 1. Plug the E5346A High density termination adapters into the FS2005/6 Analysis Probe module.

Step 2. After removing the probe tip assemblies, plug the logic analyzer cables into the E5346A interface cable headers.

<u>Logic Analyzer</u>	<u>FS2005/6</u>	<u>Comment</u>
POD 1	Header 1	Middle mictor, odd pod
POD 2	Header 2	Mictor to the left, odd pod
POD 3	Header 3	Middle mictor, even pod
POD 4	Header 4	M Clock (C4 on 1680/90), Mictor to the left, even pod
POD 5	Header 5	64 bit, Mictor to the right, odd pod
POD 6	Header 6	64 bit, Mictor to the right, even pod

Step 3. Install the FS2005/6 module into a slot in the target PCI system.

Step 4. 16500, 166x, 167x users load the logic analyzer with the appropriate file (see table below) from the 16500 FS2005/6 software diskette.

<u>Logic Analyzer</u>	<u>State/Timing Analysis</u>	<u>Description</u>
166x	CP256_3	
16554/5/6/7, 167x	CP256_2	64 bit analysis 2/555's
16554/5/6/7, 167x	CP256_1	32 bit analysis 1/555
16550	CP256_4	32 or 64 bit analysis

16600/16700 users insert the diskette labeled 16600/16700 PCI Analysis Probe Software for the FS2005/6 and install from the SYSTEM ADMINISTRATION TOOLS menu (only needs to be done once). When this has completed, load the appropriate configuration file from the **configs/FuturePlus/FS2005_6** directory on the hard disk. (see table below).

<u>Logic Analyzer</u>	<u>State/Timing Analysis</u>	<u>Description</u>
16550	CP256_7S	32 bit
1655/45/6/7, 167x	CP256_2S	64 bit
16554/5/6/7, 167x	CP256_1S	32 bit
16550, 712/3/4	CP256_4S	
16715/6/7	CP256_5S	32 bit
16715/6/7	CP256_6S	64 bit

1680/90/900 users insert the FS2005/FS2006 CD-ROM into the PC containing the 1680/90/900 Logic Analyzer software. Execute the .exe file to install the FS2005/FS2006 software. Load the configuration file from the folder that was placed on the desktop during installation.

<u>Logic Analyzer</u>	<u>State/Timing Analysis</u>	<u>Description</u>
1680/90/900	CP256_8S	32 bit
1680/90/900	CP256_9S	32/64 bit

(Cont'd on reverse)

The Configuration files provided are defined with Expander 1 below the Master, please consult your Format menu after loading a config file as to how your cards are configured.

You're ready to go!

Timing Analysis

Follow the steps on the previous page and **then** change the type field in the logic analyzer configuration menu from STATE to TIMING. **You're ready to GO!**

Test Points

The FS2005/6 module contains test point pairs (signal and ground) for all the relevant PCI Local Bus signals. These signals are labeled on the etch. Pin 1 (square pad) is the signal and Pin 2 (round pad) is the GND.

For more detailed information refer to the FS2005/FS2006 Users Manual

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