



PCI Analysis Probe FS2001

Quick Start Instructions

State Analysis

Step 1. Connect the pins of JMP1, JMP2, JMP3, and JMP4 as shown in the following table.

<u>Logic Analyzer</u>	<u>JMP1</u>	<u>JMP2</u>	<u>JMP3</u>	<u>JMP4</u>
166x, 167x, 1655x	pins 2 and 3	pins 2 and 3	pins 2 and 3	pins 2 and 3
1650, 16510 and 16540/541	pins 1 and 2	pins 1 and 2	pins 1 and 2	pins 1 and 2

Step 2. After removing the probe tip assemblies from your logic analyzer cables, plug the logic analyzer cables into the 40 pin cable headers on the FS2001 module. The table below gives the correspondence between the FS2001 cable headers and the logic analyzer cables. **16540/541 users refer to your users manual page 8.**

<u>FS2001</u>	<u>16550/166x/ 167x/ 1680/90/900 1691x</u>	<u>16554/5/6/7 16715/6/7 / 1675x, 1695x</u>	<u>Comment</u>
Header 1	Pod 1	Master Card Pod 1	
Header 2	Pod 2	Master Card Pod 2	
Header 3	Pod 3	Master Card Pod 3	L Clock (C3 for 1680/90/900)
Header 4	Pod 4	Master Card Pod 4	
Header 5	Pod 5	Expander Card Pod 1	optional 64 bit extension
Header 6	Pod 6	Expander Card Pod 2	optional 64 bit extension

The configuration files provided are defined with the Expander card below the Master. Please consult your Format Menu after loading a configuration file to see how the cards are configured.

Step 3. Install the FS2001 module into a slot in the target PCI Local Bus.

Step 4. For 16600/ 16700 users:

Insert the 16600/16700 Analysis Probe Install disk for the FS2001 into the diskette drive.

From the *System Administration Tools*, Select *Install* under *Software*. From the Software Install screen, select the *Flexible Disk* and then *Apply*. Once the title appears, select it and then select *Install*. **This procedure does not need to be repeated. It only needs to be done the first time the PCI Probe Adapter is used.**

For 1680/90/900 users:

Insert the FS-2001 CD-ROM containing the 1680/90/900 Logic Analyzer software into the PC. Execute the .exe file to install the FS2001 software. Load the configuration file from the folder that was placed on the desktop during installation. (See table below)

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Step 5. Insert the PCI Analysis Probe diskette into the Analyzer mainframe diskette drive. Load the logic analyzer with the appropriate file (see *table below*) from the Analysis Probe software diskette.

Your ready to GO!

<u>Logic Analyzer</u>	<u>State Analysis</u>	<u>Timing Analysis</u>	<u>Comment</u>
166x	F660PCI	F660PCI	
16554/5/6/7, 167x	F555PCI	F555PCI	32 bit - 4 PODS required
16554/5/6/7, 167x	F555P64	F555P64	64 bit - 6 PODS required
16550	F550PCI	F550PCI	
1650, 16510	F510PCIS	F510PCIT	
16540/541	F540PCI	F540PCI	

Note: Due to size constraints, the following files can be downloaded from the Tech Support area at www.FuturePlus.com

16715/6/7, 16750/1/2	CP201_4	CP201_4	32 bit - 4 PODS required
16715/6/7, 16750/1/2	CP201_5	CP201_5	64 bit - 6 PODS required
1680/90/900	CP201_6.	CP201_6	32 bit
1680/90/900	CP201_7	CP201_7	32/64 bit

Timing Analysis

Follow steps 1 through 5 above. In the logic analyzer configuration menu, change the type field from STATE to TIMING.

You're ready to GO!

Web: www.futureplus.com
Sales: 719-278-3540
Tech Support: 603-471-2734