



# PCI Analysis Probe and extender card FS2000

## Quick Start Instructions

### State Analysis

- Step 1.** For the Master CLK, connect pins 1 and 2 of JP1.
- Step 2.** Install the PCI add-in card into the extender card connector. Connect pins 1 and 2 with the jumper on JP2. If no add-in card is installed connect pins 2 and 3 of JP2. This will route the signals TDI/TDO to the add-in card if it is installed, or connect them together if no add-in card is installed.
- Step 3.** After removing the probe tip assemblies from your logic analyzer cables, plug the logic analyzer cables into the 40 pin cable headers on the FS2000 module. The table below gives the correspondence between the FS2000 cable headers and the logic analyzer cables. **16540/541 users refer to your users manual.**

| <u>Logic Analyzer</u> | <u>FS2000</u> | <u>Comment</u> |
|-----------------------|---------------|----------------|
| POD 1                 | Header 1      |                |
| POD 2                 | Header 2      |                |
| POD 3                 | Header 3      | L Clock        |
| POD 4                 | Header 4      |                |

- Step 4.** Install the FS2000 module into a slot in the target PCI Local Bus.
- Step 5.** If you **don't** have a 16600/16700 or a 1680/90/900 skip step 5.  
Please note: Once done, this update procedure never needs to be repeated unless a new IA revision is issued.

#### **16600/16700**

Insert the diskette labeled **16600/16700 Analysis Probe Install disk for the FS2000** into the 16600/16700 diskette drive. From the SYSTEM ADMINISTRATION TOOLS select INSTALL under SOFTWARE. From the SOFTWARE INSTALL screen select the FLEXIBLE DISK and APPLY, once the title appears select it and then select INSTALL. Load the configuration files from the /configs/FuturePlus/FS2000 directory.

#### **1680/90/900**

Insert the FS-2000 CD-ROM containing the 1680/90/900 Logic Analyzer software into the PC. Execute the .exe file to install the FS2000 software. Load the configuration file from the folder that was put on the desktop during installation.

- Step 6.** Load the logic analyzer with the appropriate file (*see table below*) from the Analysis Probe software diskette. **Your ready to GO!**

| <u>Logic Analyzer</u>  | <u>State Analysis</u> | <u>Timing Analysis</u> |
|------------------------|-----------------------|------------------------|
| 166x                   | F660PE                | F660PE                 |
| 16555, 167x            | F555PE                | F555PE                 |
| 16550                  | F550PE                | F550PE                 |
| 1650, 16510            | F510PES               | F510PET                |
| 16540/541              | F540PE                | F540PE                 |
| 16715/6/7/9, 16750/1/2 | CP200_3               | CP200_3                |
| 1680/90/900            | CP200_4               | CP200_4                |

# Timing Analysis

166x, 167x and 1655x, 1680/90 users follow the above steps and **then** change the type field in the logic analyzer configuration menu from STATE to TIMING.

**You're ready to GO!**

16510, 16540/541 and 165x users connect pins 2 and 3 of JP1, follow steps 2 thru 5 and **then** change the type field in the logic analyzer configuration menu from STATE to TIMING.

**You're ready to GO!**

## FS2000 Test Points

If the test points on your **FS2000** are configured as stake pins or blank test points use the drawings below to help locate the PCI signals.

### Test Points as seen from Side A

5V ACK64 AD01 AD03 3.3V AD08 AD10 GND CBE1 SERR PERR GND 3.3V GND AD17 AD19 GND CBE3 AD25 GND AD81 REQ CLK RSV GND RSV NID 5V TDO TCK  
5V 5V GND AD05 AD07 GND AD12 AD14 3.3V 3.3V LOCK DESEL RDY CBE2 3.3V AD21 AD23 3.3V AD27 AD29 5V GND GND GND RSN2 RSN1 INTB 5V GND -12V  
5V REQ64 AD00 GND AD06 CBE0 GND AD13 AD15 GND SDONE STOP TRDY FRAME AD16 GND AD22 IDSEL GND AD28 AD30 GND 5V RSV GND 5V 5V INTA TDI 12V  
5V 5V AD02 AD04 3.3 AD09 AD11 3.3 PAR SBO 3.3V GND GND 3.3V AD18 AD20 3.3V AD24 AD26 3.3V RSV GNT RST GND RSV RSV INTC 5V TMS TRST

### Test Points as seen from Side B

TCK TDO 5V INTD RSV GND RSV CLK REQ AD31 GND AD25 CBE3 GND AD19 AD17 GND 3.3V GND PERR SERR CBE1 GND AD10 AD08 3.3V AD03 AD01 ACK64 5V  
-12V GND 5V INTB RSN1 RSN2 GND GND GND 5V AD29 AD27 3.3V AD23 AD21 3.3V CBE2 RDY DESEL LOCK 3.3V 3.3V AD14 AD12 GND AD07 AD05 GND 5V 5V  
12V TDI INTA 5V 5V GND RSV 5V GND AD30 AD28 GND IDSEL AD22 GND AD16 FRAME TRDY STOP SDONE GND AD15 AD13 GND CBE0 AD06 GND AD00 REQ64 5V  
TRST TMS 5V INTC RSV RSV GND RST GNT RSV 3.3V AD26 AD24 3.3V AD20 AD18 3.3V GND GND 3.3V SBO PAR 3.3V AD11 AD09 3.3V AD04 AD02 5V 5V

**Warning! When installed in a powered PCI system the test point power pins are live.**

For more detailed information refer to the FS2000 Users Manual

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