PCI Analysis Probe and extender card FS2000

Quick Start Instructions

State Analysis

Step 1. For the Master CLK, connect pins 1 and 2 of JP1.

Step 2. Install the PCI add-in card into the extender card connector. Connect pins 1 and 2 with the jumper on JP2. If no add-in card is installed connect pins 2 and 3 of JP2. This will route the signals TDI/TDO to the add-in card if it is installed, or connect them together if no add-in card is installed.

Step 3. After removing the probe tip assemblies from your logic analyzer cables, plug the logic analyzer cables into the 40 pin cable headers on the FS2000 module. The table below gives the correspondence between the FS2000 cable headers and the logic analyzer cables. **16540/541 users refer to your users manual.**

Logic Analyzer	FS2000	Comment
POD 1	Header 1	
POD 2	Header 2	
POD 3	Header 3	L Clock
POD 4	Header 4	

Step 4. Install the FS2000 module into a slot in the target PCI Local Bus.

Step 5. If you **don't** have a 16600/16700 or a 1680/90/900 skip step 5.
Please note: Once done, this update procedure never needs to be repeated unless a new IA revision is issued.

16600/16700

Insert the diskette labeled **16600/16700 Analysis Probe Install disk for the FS2000** into the 16600/16700 diskette drive. From the SYSTEM ADMINISTRATION TOOLS select INSTALL under SOFTWARE. From the SOFTWARE INSTALL screen select the FLEXIBLE DISK and APPLY, once the title appears select it and then select INSTALL. Load the configuration files from the /configs/FuturePlus/FS2000 directory.

1680/90/900

Insert the FS-2000 CD-ROM containing the 1680/90/900 Logic Analyzer software into the PC. Execute the .exe file to install the FS2000 software. Load the configuration file from the folder that was put on the desktop during installation.

Step 6. Load the logic analyzer with the appropriate file (*see table below)* from the Analysis Probe software diskette. **Your ready to GO!**

Logic Analyzer	State Analysis	Timing Analysis
166x	F660PE	F660PE
16555, 167x	F555PE	F555PE
16550	F550PE	F550PE
1650, 16510	F510PES	F510PET
16540/541	F540PE	F540PE
16715/6/7/9, 16750/1/2	CP200_3	CP200_3
1680/90/900	CP200_4	CP200_4

Timing Analysis

166x, 167x and 1655x, 1680/90 users follow the above steps and **then** change the type field in the logic analyzer configuration menu from STATE to TIMING.

You're ready to GO!

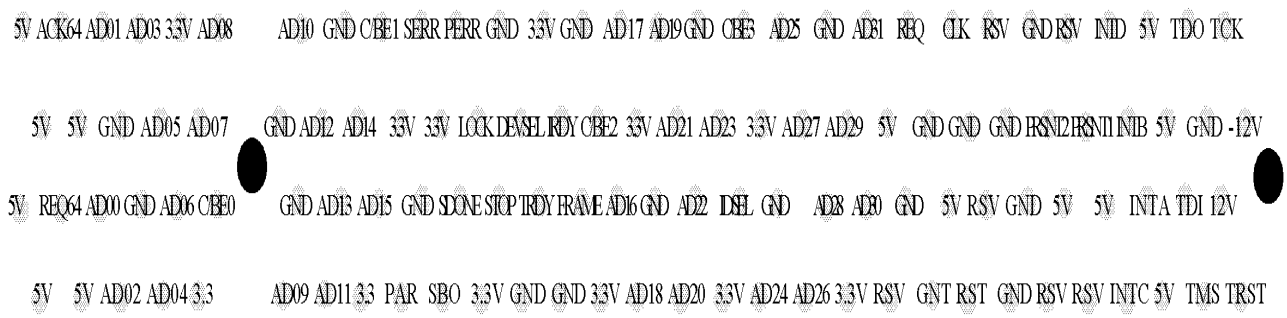
16510, 16540/541 and 165x users connect pins 2 and 3 of JP1, follow steps 2 thru 5 and **then** change the type field in the logic analyzer configuration menu from STATE to TIMING.

You're ready to GO!

FS2000 Test Points

If the test points on your FS2000 are configured as stake pins or blank test points use the drawings below to help locate the PCI signals.

Test Points as seen from Side A



Test Points as seen from Side B

Warning! When installed in a powered PCI system the test point power pins are live.

For more detailed information refer to the FS2000 Users Manual

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