

Embedded DDR Probe Software

FS1107

Quick Start Instructions

Step 1. If you are using an IC probe, follow it's instructions to wire the logic analyzer's flying leads to the correct pins on the probe. Consult the manual for the pod assignments. Also for the embedded application consult the manual for different card configurations. For IC probe Configure 2 cards together as one analyzer, configure such that the expander is below the master, install another card as a single master in the third slot (this will be called master 2). Connect the logic analyzer cards to the headers of the probe as follows:

<u>Logic Analyzer</u>	<u>Probe header</u>	<u>Analyzer</u>
Master POD 1	Header 1	Data
Master POD 2	Header 2	Data
Expander POD 1	Header 3	Data
2 nd Master POD 1	Header 4	Command
2 nd Master POD 2	Header 5	Command

Step 2. Install the probe onto the target device per instructions.

Step 3. Install the FS1107 software. To install the FS1107 software insert the diskette labeled **16700 Installation disk for the FS1107** into the Agilent 16700/702 diskette drive. From the SYSTEM ADMINISTRATION TOOLS select *INSTALL* under *SOFTWARE*. From the SOFTWARE INSTALL screen select the *FLEXIBLE DISK* and *APPLY*. Once the title appears select it and then select *INSTALL*. Follow the above procedure to install the DDR filter tool. Please consult manual for the operation of the filter tool.

Step 4. Load the logic analyzer with the correct file from the logic/configs/futureplus/FS1107 directory. Load the system file corresponding to one of the files listed below.

Logic Analyzer Cards and probe	File-reads	File-writes	Comments
16715/6/7/9 16750/1/2 with an IC Probe	DR1107_1	DR1107_2	3 card system config files for use with IC probe
16715/6/7/9 16750/1/2 with Mictors	DR1107_3	DR1107_4	3 card system config files for use with Mictor connector
16715/6/7/9 16750/1/2	DR1107_5	DR1107_6	2 card config files for 200Mhz analysis for use with Mictor connector
16760/1675x	DR1107_7	DR1107_7	Use with IC probe, need 3 16760 cards
16760/1675x	DR1107_8	DR1107_8	Embedded config. Needs 4 16760 cards, 1 1675x
16760/1675x	DR1107_9	DR1107_9	Same as DR1107_8, but for bus speeds > 400Mb/s

Timing Analysis

To perform timing analysis nothing has to be done to the probe. Timing analysis is performed by loading either configuration file and changing from state to timing. To change to timing, after loading the configuration file, go to the **sampling** tab of the **format** menu and select timing. Do this for both the Command and Data Analyzers.

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