

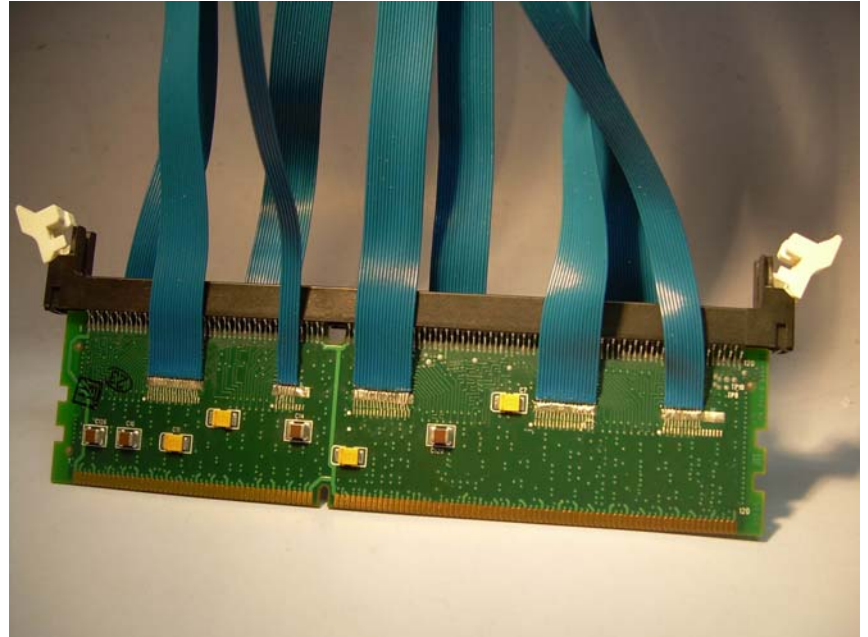
# DDR3 1333 / 1600 Interposer

For use with Tektronix Logic Analyzers

FuturePlus® Systems

Power Tools for Bus Analysis

- DDR3 1333 / 1600 analysis
- Supports Tektronix TLA logic analyzers
- Includes protocol-decode software and automatic logic analyzer setup software
- Interposer design does not sacrifice a slot!



## FS2358 DDR3 1333 / 1600 Interposer

### Key Features

- Quick connection between a DDR3 DIMM memory bus connector and Tektronix logic analyzers
- Complete and accurate 1333-1600 MT/s state analysis
- Supports 240-pin DDR3 SDRAM DIMM's up to 1600 MT/s
- All signals are probed passively
- Registered and unbuffered DIMMs are supported.
- Burst sizes of 4 or 8 are supported
- Monitors writes only, reads only, or writes and reads
- Read and write burst type is tracked
- Each cycle of a burst is flagged by the TLA as a read or write
- Selective Cloning stores only transactions of interest
- Conditional storage allows for store only mod-register commands

### Straightforward, Reliable DDR3 1333 / 1600 Analysis

The FuturePlus FS2358 DDR3 1333 / 1600 Interposer provides a mechanical, electrical and software interface between an Tektronix logic analyzer and the DDR3 connector. The FS2358 is used to design and debug computer motherboards and DIMM's incorporating DDR3 technology.



### Helping you Design Tomorrow's Computers, Today

FuturePlus Systems is the technology leader in protocol analysis tools for the computer design industry. Our Interposers and software help you monitor and verify complex activities on your advanced-technology computer bus design. FuturePlus systems offerings include bus-analysis solutions for most popular computer buses. Visit [www.futureplus.com](http://www.futureplus.com) for more information.

**Tektronix**

Embedded Systems  
Tools Partner





## Ordering Information

**FS2358** – DDR3 1333 / 1600 Interposer for use with Tektronix Logic Analyzers

**Software included with the FS2358:**

**FS1162** Setup files for the Tektronix logic analyzer and Protocol Decoder software, runs on the Tektronix logic analyzer

**Target Systems Running at 1333 MB/s:**

The following are required to connect the FS2358 to the TLA for 1333 MT/s operation:

**FS1061** – HCD TLA7BB4 Full Channel Probe Cable – 1 ea

**FS1062** – HCD TLA7BB4 Half Channel Probe Cable – 3 ea

**Target Systems Running at 1600 MB/s:**

The following are required to connect the FS2358 to the TLA for 1600 MT/s operation:

**FS1061** – HCD TLA7BB4 Full Channel Probe Cable – 1 ea

**FS1169** – DDR3 1600 Software Support Package – 1 ea

**P6962HCD** – Probe Cable, available from Tektronix – 3 ea

### Logic Analyzer Requirements

- **Target Systems Running at 1333 MB/s:**

TLA7BB4 cards – 2 ea

- **Target Systems Running at 1600 MB/s:**

TLA7BB4 cards – 4 ea



See [http://www.futureplus.com/products/fs2358/fs2358\\_sysreq.shtml](http://www.futureplus.com/products/fs2358/fs2358_sysreq.shtml) for detailed requirements.

#### FuturePlus Systems Corporation

P.O. Box 88155  
Colorado Springs, CO 80908-8155  
Tel: 719 278 3540  
Fax: 603 471 2738  
Website: [www.futureplus.com](http://www.futureplus.com)

#### Represented By: