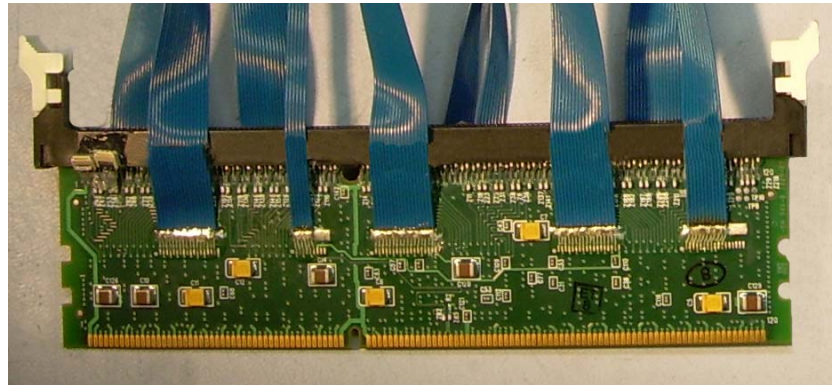


# DDR3 DIMM 2133 Interposer **FuturePlus Systems**

For use with Agilent Logic Analyzers

**Power Tools For Bus Analysis**

- DDR3 2133 MT/s bus analysis
- Supports Agilent 16900-series and U4154A logic analyzers
- Includes protocol-decode software, probe configuration software, and automatic logic analyzer configuration software
- Optional Protocol Compliance and Analysis software available
- Interposer design does not consume a slot
- Includes Clock Qualifier feature if needed to allow accurate testing in 2 rank systems



**FS2352B DDR3 DIMM 2133 Interposer**



## Key Features

- Quick and easy connection between the DDR3 DIMM SDRAM memory bus connector and Agilent logic analyzers
- Complete and accurate 2133 MT/s state and timing analysis up to 12.5 GHz
- Compatible with all 240-pin DDR3 SDRAM DIMM's up to 2133 MT/s.
- All signals are probed passively.
- Does not require termination adapters; they are built-in
- Registered, unbuffered, and large register DIMMs are supported.
- Burst sizes of 2, 4, or 8 are supported.
- Monitors writes only, reads only, or writes and reads.
- Quick and easy setup using Agilent Eye Scan with 5 ps resolution

## Straightforward, Reliable DDR3 2133 Analysis

The FuturePlus® FS2352B DDR3 DIMM 2133 Interposer provides a mechanical, electrical and software interface between an Agilent logic analyzer and the DDR3 2133 connector. The FS2352B is used to design and debug computer motherboards and DIMM's incorporating DDR3 2133 technology.

## Helping you Design Tomorrow's Computers, Today

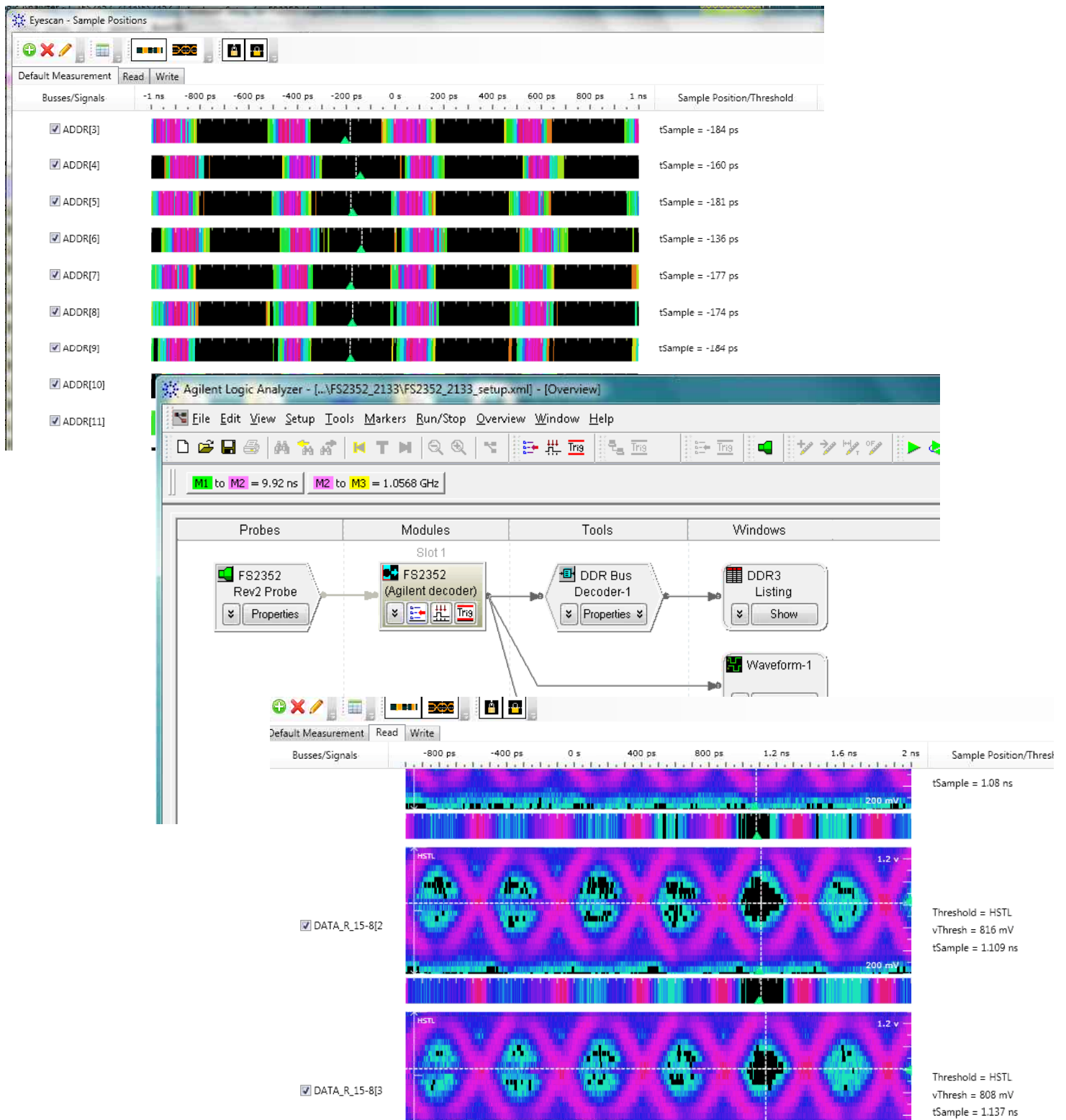
FuturePlus Systems is the technology leader in protocol analysis tools for the computer design industry. Our Interposers and software help you monitor and verify complex activities on your advanced technology computer bus design. FuturePlus systems offerings include bus-analysis solutions for most popular computer buses. Visit [www.futureplus.com](http://www.futureplus.com) for more information.





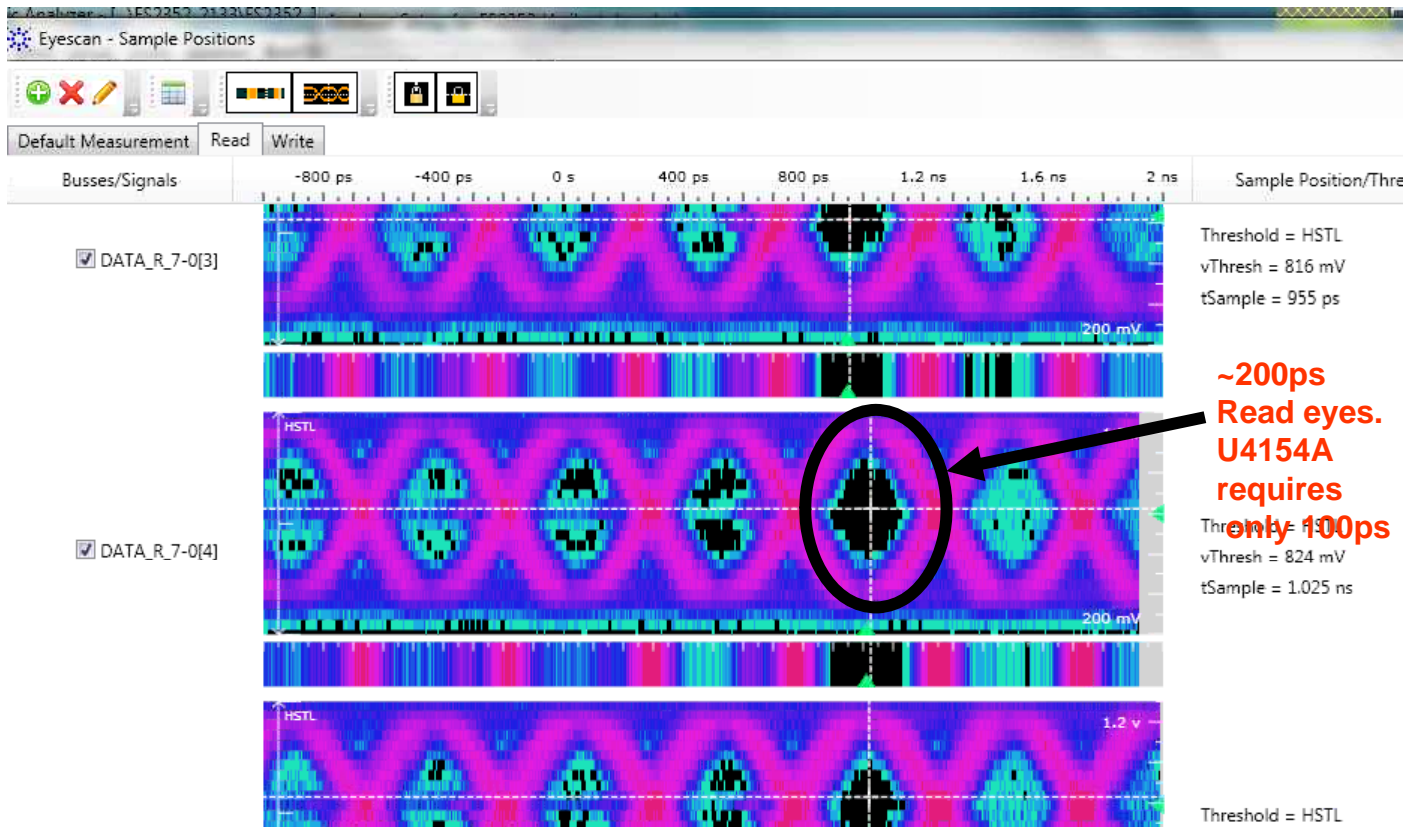
## Quick and Accurate Setup

Quickly gain signal integrity insight with Agilent EyeScan technology. As timing and voltage margins continue to shrink, confidence in signal integrity becomes an increasingly vital requirement of the design verification process. EyeScan lets you quickly acquire comprehensive signal integrity information on the DDR3 bus in your design, and can provide measurements with 5 ps of resolution.

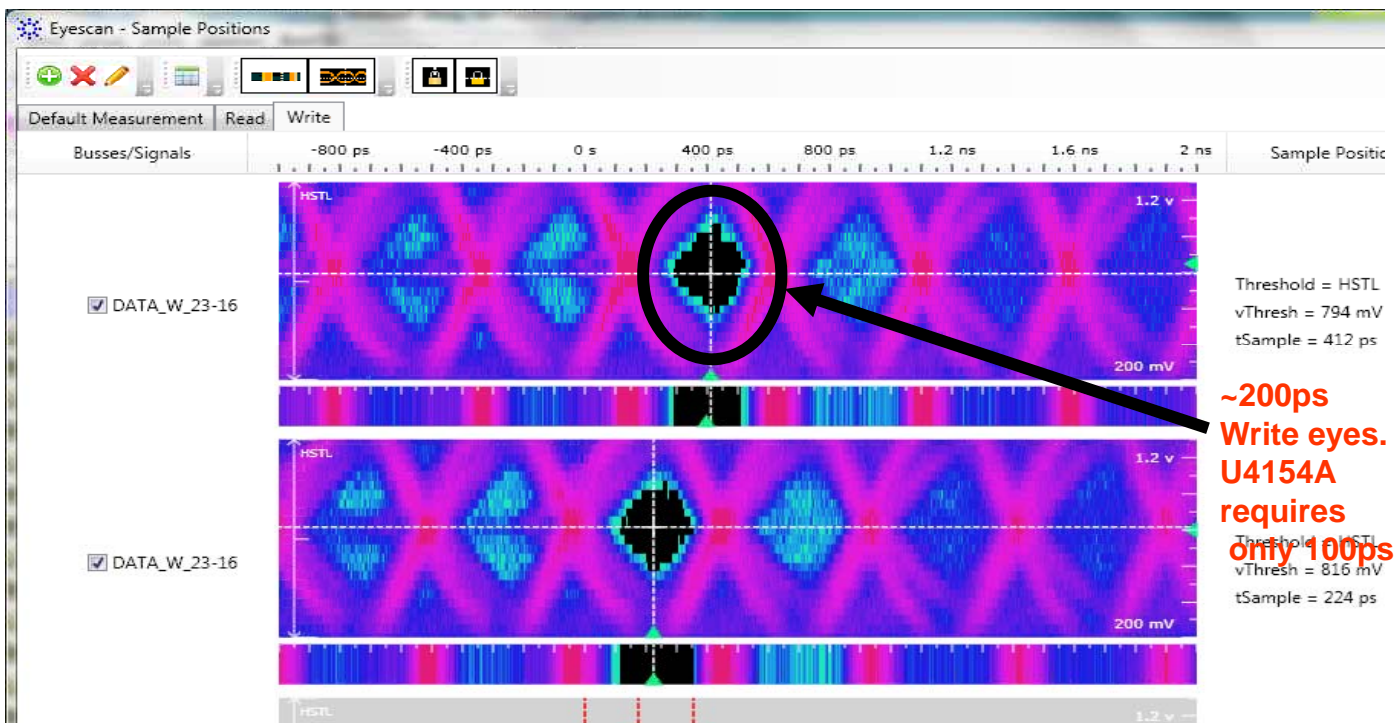


# Demonstrated 2133 operation

## DDR3 READ Eyes from Eyescan Display on the FS2352B

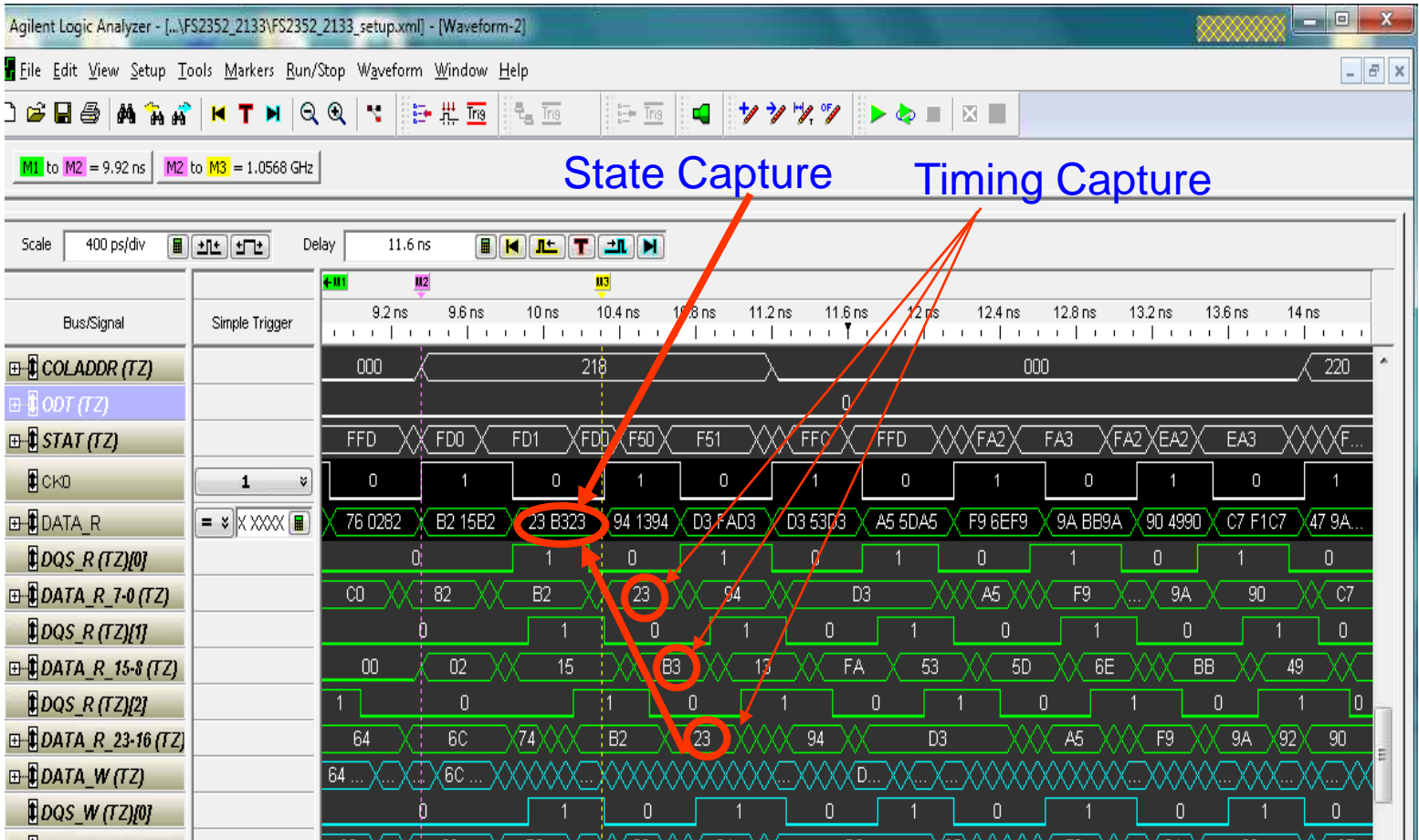


## DDR3 WRITE Eyes from Eyescan Display on the FS2352B

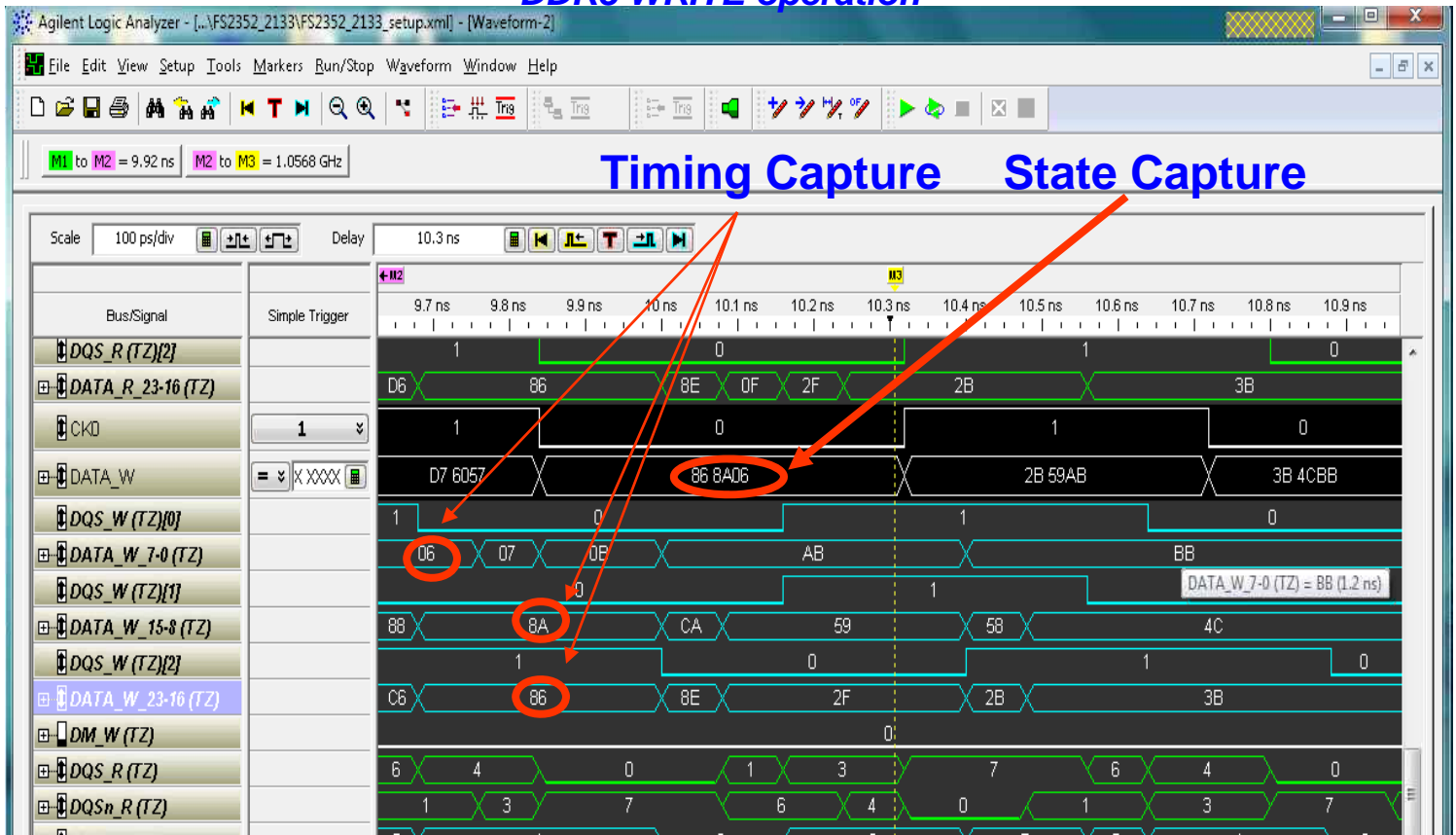


# 2133 State Capture Verified

## DDR3 READ operation



## DDR3 WRITE operation



# Ordering Information

**FS2352B** – DDR3 2133 Interposer for use with Agilent Logic Analyzers

**Software included with the FS2133:**

- Configuration files for the Agilent logic analyzer
- Protocol Decoder software, runs on the Agilent logic analyzer

**Optional Software Available from Agilent Technologies:**

- B4622A DDR3 Protocol Compliance and Analysis Tool

## Agilent Logic Analyzer Requirements

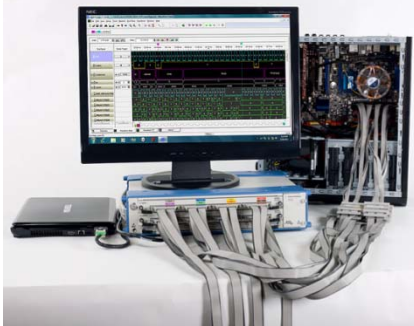
For 2133 operation, the FS2352B requires the U4154A. For 1867 and below, either the U4154A or the 16962A can be used.

**The FS2352B requires**

- 1 ea M9502A two-slot AXIe chassis
- 2 ea U4154A 136-channel Logic Analyzer Modules
- 4 ea U4201A Probe Cables

**OR**

- 1 ea 16902B Modular Logic Analysis System
- 4 ea 16962A 68-Channel. 4 GB/s State Logic Analyzer Modules



M9502A with U4201A Probe Cables



U4154A



16902B



16962A

### FuturePlus Systems Corporation

P.O. Box 88155  
Colorado Springs, CO 80909-8155  
Tel: 719 278 3540  
Fax: 719 278-9586  
Website: [www.futureplus.com](http://www.futureplus.com)

### Represented By: